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- (C) self.assertEqual(len(ids), 1) Note canonicalized names self.assertEqual(GObject.signal\_name(ids 0), 'my-signal') There is no signal 0 in gobject self.a

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- · Copyright (C) 2013 Golden Delicious Computers Lukas Märdian Based on dumb driver for gta01 battery
- Copyright (C) 2013 Golden Delicious Computers Nikolaus Schaller
- Copyright (C) 2013 Google Inc. Author: Willem de Bruijn (willemb@google.com) A basic test of packet socket fanout behavior
- Copyright (C) 2013 Google Inc. Author: Willem de Bruijn Daniel Borkmann
- Copyright (C) 2013 Google Inc. Written by Willem de Bruijn
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  checking its VMX registers, at some p
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  happened so this test just uses many thr
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- Copyright (C) 2017 Gustavo Romero, IBM Corp. Check if thread endianness is flipped inadvertently to BE on trap caught in TM whilst MSR.FP and MSR.VEC are zero (i.e. just after load\_fp and loa
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- Copyright (C) 2017-2018 Hannes Reinecke, SUSE Linux GmbH Based on the original DAC960 driver, which has
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- Copyright (C) 2017 IBM Corp. include include include include include include include
- Copyright (C) 2017,2019 IBM Corp. include include
- Copyright (C) 2017 IBM Corp. include
- Copyright (C) 2017 IBM Corporation cpufeatures is the new way to discover CPU features with /cpus/features devicetree. This supersedes PVR based discovery (
- · Copyright (C) 2017 IBM Corporation. This uses code from arch/sparc/kernel/nmi.c and kernel/watchdog.c
- Copyright (C) 2017 IBM Corp. undef TRACE\_SYSTEM define TRACE\_SYSTEM ocxl
- Copyright (C) 2017 Impinj, Inc Author: Andrey Smirnov Based on the code of analogus driver:
- Copyright (C) 2017 Joel Stanley

- Copyright (C) 2017 John Sperbeck Test that an access to a mapped but inaccessible area causes a SEGV and reports si\_code == SEGV\_ACCERR
- · Copyright (C) 2017 Keith Packard include include
- Copyright (C) 2017 Manivannan Sadhasivam IIO driver for VL6180 (7-bit I2C slave address 0x29)
- Copyright (C) 2017 Maxime Roussin-B expanded by Silvan Murer
- . Copyright (C) 2017 Michael Neuling, IBM Corp. Original: Breno Leitao & Gustavo Bueno Romero Edited: Michael Neuling Force VMX unavailabl
- · Copyright (C) 2017 Nicholas Piggin, IBM Corporation
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- Copyright (C) 2017,2020 Nuvoton Technology Crop
- Copyright (C) 2017 Nuvoton Technology Crop. Author: David Lin Co-author: John Hsu Co-author: Seven Li
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- Copyright (C) 2017 Tim Harvey
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- Copyright (C) 2018-2019 NXP Based on caamalg.c crypto API driver
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- · Copyright (C) 2018 Breno Leitao, Gustavo Romero, IBM Corp. A test case that creates a signal and starts a suspended transaction inside the signal handler
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- Copyright (C) 2018 Cirrus Logic, Inc. Author: James Schulman
- Copyright (C) 2018 David Lechner Based on mi0283qt.c:

- · Copyright (C) 2018 DEVIALET
- Copyright (C) 2018 Google Inc. Author: Eric Dumazet (edumazet@google.com) Reference program demonstrating tcp mmap() usage, and SO\_RCVLOWAT hints for receiver
- Copyright (C) 2018 Google Inc. Author: Soheil Hassas Yeganeh (soheil@google.com) Simple example on how to use TCP\_INQ and TCP\_CM\_INQ
- Copyright (C) 2016,2018 Google, Inc This driver uses the cros-ec
- Copyright (C) 2018,2020 Google LLC Author: Eric Biggers
- Copyright (C) 2018 Google LLC. Author: Tomasz Figa Author: Yong Zhi
- · Copyright (C) 2018 Google LLC. Author: Tuukka Toivonen Author: Sakari Ailus Author: Samu Onkalo Author: To
- Copyright (C) 2018 Google LLC. Driver for Semtech's SX9310/SX9311 capacitive proximity/button solution. Based on SX9500 driver and Semtech driver
  using the input framework https://my.syncplic
- · Copyright (C) 2018 Google LLC The corresponding platform device is typically registered in drivers/platform/chrome/wilco\_ec/core.c
- Copyright (C) 2018 Google LLC The Wilco EC is similar to a typical ChromeOS embedded controller. It uses the same MEC based low-level communication
  and a similar protocol, but with some import
- . Copyright (C) 2018 Google LLC This is the entry point for the drivers that control the Wilco EC
- Copyright (C) 2014,2018 Google LLC. Tomasz Figa
- Copyright (C) 2018 Google LLC. Tomasz Figa Based on s5p-mfc driver by Samsung Electronics Co., Ltd
- . Copyright (C) 2018 IBM Corp A FSI master controller, using a simple GPIO bit-banging
- Copyright (C) 2018 IBM Corporation Author: Anshuman Khandual
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- Copyright (C) 2018 Intel Corporation Author: Gaurav K Singh Manasi Navare
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- Copyright (C) 2018 Marty E. Plummer
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- Copyright (C) 2018 SUSE Linux. Author: Coly Li include include
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- Copyright (C) 2019-2020 NXP Author: Subrahmanya Lingappa Hou Zhiqiang
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- Copyright (C) 2019 Arm Limited Authors: Mark Rutland Amit Daniel Kachhap
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- . Copyright (C) 2019 Collabora ltd. include include include include include include include
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- Copyright (C) 2019 Collabora ltd. include include include include include include in
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- Copyright (C) 2019 Dan Robertson I2C address is either 0x14 or 0x15 depending on SDO
- Copyright (C) 2019 Dan Robertson TODO:
- Copyright (C) 2019 dvanced Micro Devices, Inc
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- · Copyright (C) 2019 Google LLC See Documentation/ABI/testing/sysfs-
- Copyright (C) 2019 Google LLC Since the EC will never change the backlight level of its own accord, we don't need to implement a brightness\_get() method
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- Copyright (C) 2019 Google LLC The Wilco Embedded Controller can create custom events that are not handled as standard ACPI objects. These events can contain information about changes in EC con
- Copyright (C) 2019 Google LLC The Wilco Embedded Controller is able to send telemetry data which is useful for enterprise applications. A daemon running on the OS sends a command to the EC via
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- Copyright (C) 2019 Gustavo Romero, Michael Neuling, IBM Corp. This test will spawn two processes. Both will be attached to the same CPU (CPU 0). The
  child will be in a loop writing to FP registe
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- Copyright (C) 2019 IBM Corporation Eddie James
- Copyright (C) 2019 Inspur Corp
- Copyright (C) 2017-2020 Intel Corporation, Inc. Authors:
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- Copyright (C) 2019 Linaro Ltd. Author Lee Jones
- Copyright (C) 2019 Linaro, Ltd, Rob Herring
- Copyright (C) 2019 Linaro, Ltd., Rob Herring
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- Copyright (C) 2019 Nick Piggin, Gautham R. Shenoy, Aneesh Kumar K.V, IBM Corp
- Copyright (C) 2019 NXP Author: Daniel Baluta
- Copyright (C) 2019 NXP Author: Daniel Baluta Implementation of the DSP IPC
- Copyright (C) 2019 NXP Based on the barebox ocotp driver
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- Copyright (C) 2019 NXP. Scaling algorithms were contributed by Dzung Hoang
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  vlan\_filtering bridge. It does this by using th
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- . Copyright (C) 2020 Brian Geffon define \_GNU\_SOURCE include include include include include include
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- Copyright (C) 2020 Google LLC. include
- Copyright (C) 2020 Google LLC. Julius Werner Based on mtk\_wdt.c
- Copyright (C) 2020 Google LLC This driver provides the ability to view and manage Type C ports through the Chrome OS EC
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- Copyright (C) 2020 GRATE-driver project
- Copyright (C) 2020 GRATE-driver project. Based on downstream driver from Acer Inc. Based on NVIDIA Gas Gauge driver for SBS Compliant Batteries
- Copyright (C) 2020 Gustavo Luiz Duarte, IBM Corp. This test starts a transaction and triggers a signal, forcing a pagefault to happen when the kernel signal handling code touches the user signal
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- · Copyright (C) 2020 Sandipan Das, IBM Corp. Test if applying execute protection on pages using memory protection keys works as expected
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- Copyright (C) 2005-2021 Intel Corporation include include include
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- Copyright (C) 2021 NXP clock driver for Freescale QorlQ SoCs
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- Copyright (C) 1991-1994 Linus Torvalds Dave Engebretsen Rework for PPC64 port
- Copyright (C) 1991-1994 Linus Torvalds Modified by Cort Dougan and Paul Mackerras
- Copyright (C) 1991-1994 Linus Torvalds Modified by Fred N. van Kempen, 01/29/93, to add line disciplines which can be dynamically activated and de-activated by the line disci
- Copyright (C) 1991-1994 Linus Torvalds Swap reorganised 29.12.95, Asynchronous swapping added 30.12.95. Stephen Tweedie Removed race in async swapping. 14.4.1996. Bruno H
- Copyright (C) 1991-1994 Linus Torvalds Swap reorganised 29.12.95, Stephen Tweedie
- Copyright (C) 1991-1994 Linus Torvalds Swap reorganised 29.12.95, Stephen Tweedie. kswapd added: 7.1.96 sct Removed kswapd\_ctl limits, and swap out as many pages as neede
- Copyright (C) 1991-1994 Linus Torvalds Swap reorganised 29.12.95, Stephen Tweedie Support of BIGMEM added by Gerhard Wichert, Siemens AG, July 1999 Reshaped it to be a zoned
- Copyright (C) 1991-1994 Linus Torvalds zoned VM statistics
- Copyright (C) 1991-1992,1995,1999-2000 Linus Torvalds This file contains date handling
- Copyright (C) 1991-1992,1995,1999-2000 Linus Torvalds This file contains the clocksource time handling. 1997-09-10 Updated NTP code according to technical memorandum Jan '96
- Copyright (C) 1991-1992,1995 Linus Torvalds Adapted for PowerPC (PReP) by Gary Thomas Modified by Cort Dougan (cort@cs.nmt.edu). Copied and
  modified from arch/i386/kernel/time.c
- Copyright (C) 1991-1992,1995 Linus Torvalds This file contains the m68k-specific time handling details. Most of the stuff is located in the machine specific
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- Copyright (C) 1991-1992,1996 Linus Torvalds linux/fs/binfmt\_flat.c for 2.0 kernel
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- Copyright (C) 1991-1992 Linus Torvalds 1995 Jay Estabrook User definable mapping table and font loading by Eugene G. Crosser
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- Copyright (C) 1991-1992 Linus Torvalds 64-bit file support on 64-bit platforms by Jakub Jelinek (jj@sunsite.ms.mff.cuni.cz)
- Copyright (C) 1991-1992 Linus Torvalds Added devfs support. Jan-11-1998, C. Scott Ananian Shared /dev/zero mmapping support, Feb 2000, Kanoj Sarcar
- Copyright (C) 1991-1992 Linus Torvalds Added support for a Unix98-style ptmx device. -- C. Scott Ananian , 14-Jan-1998
- Copyright (C) 1991-1992 Linus Torvalds adfs regular file handling primitives
- Copyright (C) 1991-1992 Linus Torvalds affs symlink handling code
- . Copyright (C) 1991-1992 Linus Torvalds Based on bootsect.S and setup.S modified by more people than can be counted
- Copyright (C) 1991-1992 Linus Torvalds Big-endian to little-endian byte- swapping/bitmaps by David S. Miller (davem@caip.rutgers.edu), 1995
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- Copyright (C) 1991-1992 Linus Torvalds changes by Thomas Schoebel-Theuer
- Copyright (C) 1991-1992 Linus Torvalds coh/dir.c
- Copyright (C) 1991-1992 Linus Torvalds coh/file.c
- Copyright (C) 1991-1992 Linus Torvalds coh/namei.c
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- . Copyright (C) 1991-1992 Linus Torvalds entry\_32.S contains the system-call and low-level fault and trap handling routines
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- Copyright (C) 2014 Robert Jarzmik Common clock code for PXA clocks (
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- · Copyright (C) 2014 Samsung Electronics Co., Ltd. Author: Rahul Sharma Provides Constants for Exynos5260 clocks
- Copyright (C) 2014 Samsung Electronics Co., Ltd. Author: Thomas Abraham
- Copyright (C) 2014 Samsung Electronics Co., Ltd. Author: Thomas Abraham A CPU clock is defined as a clock supplied to a CPU or a group of CPUs. The CPU clock is typ
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- Copyright (C) 2014 Samsung Electronics Co., Ltd. This driver is based on the
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- Copyright (C) 2019 Facebook ----- START-EXPECTED-OUTPUT ----- struct s1 {};
- Copyright (C) 2019 Facebook ----- START-EXPECTED-OUTPUT ----- struct S { int S; int U;
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- Copyright (C) 2019 Tomasz Figa Loosely based on old fsa9480 misc-device driver
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- (C) Adam Belay. include include include include include include include linux/serial\_core
- · (C) Alex Holden
- (C) and server (S): Setup of client connection:
- (C) Architecture Software Developer's Manual, Volume 3: System Prgramming Guide, revision 12 in Table B-1: MSRs in the Pentium 4 and Intel Xeon Processors, on page B-4 and B-5. fsb\_
- (C) A(W1) C(C) Which would create W1-C-W1 dependencies, even though there is no actual deadlock possible. There are two solutions, using a
  read-recursive acquire on the wor
- (C) Balbir Singh, IBM Corp. 2006
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- (C) bits in a HPT entry can be set by hardware at any time up until the HPTE is invalidated and the TLB invalidation sequence has completed. This means that when removing a HPTE
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- Copyright (C) 2015 Michael Neuling, IBM Corp. Original: Michael Neuling 4/12/2013 Edited: Rashmica Gupta 4/12/2015
- Copyright (C) 2015 Michael Neuling, IBM Corp. Test the kernel's signal delievery code to ensure that we don't trelaim twice in the kernel signal delivery code.
   This can happen if we trigger
- Copyright (C) 2015 Michael Neuling, IBM Corp. Test the kernel's signal return code to ensure that it doesn't crash when both the transactional and suspend MSR bits are set in the signal conte
- Copyright (C) 2015 Nouveau project
- Copyright (C) 2015 Nuvoton Technology Corp. Co-author: Meng-Huang Kuo
- Copyright (C) 2014-2015 Pengutronix, Markus Pargmann
- Copyright (C) 2015 Realtek Microelectronics Author: Oder Chiou
- Copyright (C) 1982,1986,1989,2012-2016 Regents of the University of California
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- Copyright (C) 2008,2011,2014-2017 Robert Jarzmik
- Copyright (C) 2015 Sam Bobroff, IBM Corp. Test the kernel's system call code to ensure that a system call made from within an active HTM transaction is aborted with the correct failure code
- Copyright (C) 2015 Samuel Pitoiset
- · Copyright (C) 2015 Samuel Pitosiet
- Copyright (C) 2015 Simon Arlott Derived from bcm63138\_nand.c:
- Copyright (C) 2015 Simon Arlott This is based on the BCM7038 (which supports SMP) but with a single enable register instead of separate mask/set/clear registers
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- Copyright (C) 2015,2018 SUSE Linux GmbH
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- Copyright (C) 2015 Verifone Int. Author: Nicolas Saenz Julienne
- Copyright (C) 2015 www.starterkit.ru Based on:
- Copyright (C) 2016 17 IBM Corp
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- . Copyright (C) 2016-2020 NXP include include include include include include
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- Copyright (C) 2016 ArcticSand, Inc. Author: Brian Dodge
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- Copyright (C) 2016 Chen-Yu Tsai Based on the Allwinner SDK driver
- Copyright (C) 2016 Chris Smart, IBM Corporation. Calls to copy\_first which are not 128-byte aligned should be caught and sent a SIGBUS
- Copyright (C) 2016 Christophe Leroy, CS Systemes d
- Copyright (C) 2016-2017 Cirrus Logic, Inc. Author: Brian Austin
- · Copyright (C) 2016 Cirrus Logic, Inc. Author: James Schulman Author: Brian Austin Author: Michael White
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- · Copyright (C) 2016 Cyril Bur, IBM Corp. Sending one self a signal should always get delivered
- · Copyright (C) 2016 Cyril Bur, IBM Corp. Syscalls can be performed provided the transactions are suspended. The exec()
- Copyright (C) 2016 Cyril Bur, IBM Corp. Test the kernel's signal frame code
- Copyright (C) 2016 David Lechner Based on iio/ad7923.c:
- Copyright (C) 2016 Elettra-Sincrotrone Trieste S.C.p.A. Author: Alessio Igor Bogani
- Copyright (C) 2012-2014,2016 Ezequiel Garcia
- · Copyright (C) 2016 Ezequiel Garcia Based on LED IDE-Disk Activity Trigger
- Copyright (C) 2014,2016-2017 Free Electrons Author: Boris Brezillon
- · Copyright (C) 2016 Free Electrons NextThing Co. Quentin Schulz This driver is based on a previous upstreaming attempt by:
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- Copyright (C) 2016 Google Inc. include include include include include include
- Copyright (C) 2016 Hans Verkuil hverkuil@xs4all.nl
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- Copyright (C) 2016 Icenowy
- Copyright (C) 2016-2020 Icenowy Zheng
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- Copyright (C) 2016 Linaro Ltd. define pr\_fmt(fmt) KBUILD\_MODNAME
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- Copyright (C) 2016 Nuvoton Technology Corp. Author: David Lin
- Copyright (C) 2016 Nuvoton Technology Corp. Author: John Hsu
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- Copyright (C) 2004,2008-2009,2013,2016 Pengutronix
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- Copyright (C) 2016 Qtechnology A/S 2016 Ricardo Ribalda
- Copyright (C) 2016 Qtechnology A/S 2016 Ricardo Ribalda include include include include include linux
- Copyright (C) 2016 Rashmica Gupta, IBM Corp
- Copyright (C) 2016 Rashmica Gupta, IBM Corp. This traverses the kernel pagetables and dumps the information about the used sections of memory to /sys/kernel/debug/kernel\_pagetables

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- · Copyright (C) 2017 Anshuman Khandual, IBM Corp. Works on architectures which support 128TB virtual address range and beyond
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- Copyright (C) 2017 Author: Dong Aisheng File containing client-side RPC functions for the MISC service. These function are ported to clients that communicate to the SC
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- Copyright (C) 2017 by Helge Deller possible future enhancements:
- Copyright (C) 2017 Cadence Design Systems Inc. include include include include include include include linux
- Copyright (C) 2017 Canonical Ltd. TODO If a task uses change\_hat it currently does not return to the old cred or task context but instead creates a new one. Ideally the task should return
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- Copyright (C) 2017 Cirrus Logic, Inc. Authors: Li Xu
- Copyright (C) 2017 (c) Samsung Electronics Software, Inc. Based on the Exynos PRNG driver drivers/crypto/exynos-rng by Krzysztof Koz

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- Copyright (C) 2016-2019 David Lechner
- Copyright (C) 2017 David Lechner Some code copied from mipi-dbi.c
- · Copyright (C) 2017 Dong Aisheng
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- Copyright (C) 2017 Google, Inc This driver uses the memory mapper cros-ec
- · Copyright (C) 2017 Google LLC Based on Intel IPU4 driver
- Copyright (C) 2017 Gustavo Romero, Breno Leitao, Cyril Bur, IBM Corp. Force FP, VEC and VSX unavailable exception during transaction in all possible scenarios regarding the MSR.FP and MSR.VEC st
- Copyright (C) 2017 Gustavo Romero, IBM Corp. Check if thread endianness is flipped inadvertently to BE on trap caught in TM whilst MSR.FP and MSR.VEC are zero (i.e. just after load fp and loa
- · Copyright (C) 2017 Hannes Reinecke, SUSE Linux GmbH Based on the original DAC960 driver
- . Copyright (C) 2017-2018 Hannes Reinecke, SUSE Linux GmbH Based on the original DAC960 driver, which has
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- Copyright (C) 2017 IBM Corp. include include include include include include asm/r
- Copyright (C) 2017 IBM Corp. include include include include include include include
- Copyright (C) 2017,2019 IBM Corp. include include
- Copyright (C) 2017 IBM Corp. include
- Copyright (C) 2017 IBM Corporation cpufeatures is the new way to discover CPU features with /cpus/features devicetree. This supersedes PVR based discovery (

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- Copyright (C) 2017 IBM Corp. undef TRACE\_SYSTEM define TRACE\_SYSTEM ocxl
- Copyright (C) 2017 Impinj, Inc Author: Andrey Smirnov Based on the code of analogus driver:
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- · Copyright (C) 2017 Keith Packard include include
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- Copyright (C) 2017 Nuvoton Technology Crop. Author: David Lin Co-author: John Hsu Co-author: Seven Li
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- Copyright (C) 2018-2019 NXP Based on caamalg.c crypto API driver
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- Copyright (C) 2018-2019 NXP. Limitations:
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- Copyright (C) 2018 Bharata B Rao, IBM Corp
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- Copyright (C) 2018 DEVIALET
- Copyright (C) 2018 Google Inc. Author: Eric Dumazet (edumazet@google.com) Reference program demonstrating tcp mmap() usage, and SO\_RCVLOWAT hints for receiver
- Copyright (C) 2018 Google Inc. Author: Soheil Hassas Yeganeh (soheil@google.com) Simple example on how to use TCP\_INQ and TCP\_CM\_INQ
- Copyright (C) 2016,2018 Google, Inc This driver uses the cros-ec
- Copyright (C) 2018,2020 Google LLC Author: Eric Biggers
- Copyright (C) 2018 Google LLC. Author: Tomasz Figa Author: Yong Zhi
- · Copyright (C) 2018 Google LLC. Author: Tuukka Toivonen Author: Sakari Ailus Author: Samu Onkalo Author: To
- Copyright (C) 2018 Google LLC. Driver for Semtech's SX9310/SX9311 capacitive proximity/button solution. Based on SX9500 driver and Semtech driver
  using the input framework https://my.syncplic
- Copyright (C) 2018 Google LLC The corresponding platform device is typically registered in drivers/platform/chrome/wilco\_ec/core.c
- Copyright (C) 2018 Google LLC The Wilco EC is similar to a typical ChromeOS embedded controller. It uses the same MEC based low-level communication
  and a similar protocol, but with some import
- . Copyright (C) 2018 Google LLC This is the entry point for the drivers that control the Wilco EC
- Copyright (C) 2014,2018 Google LLC. Tomasz Figa
- Copyright (C) 2018 Google LLC. Tomasz Figa Based on s5p-mfc driver by Samsung Electronics Co., Ltd
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- Copyright (C) 2018 IBM Corporation Author: Anshuman Khandual
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- . Copyright (C) 2018 Lubomir Rintel include include include include include include
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- Copyright (C) 2018 Marty E. Plummer
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- · Copyright (C) 2019 ARM Limited 32 Bit compat layer by Stefani Seibold sponsored by Rohde & Schwarz GmbH & Co. KG Munich/Germany
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- Copyright (C) 2019 Collabora ltd. include include include include include include include
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- . Copyright (C) 2019 Collabora ltd. include include include include include include in
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- Copyright (C) 2019 Dan Robertson I2C address is either 0x14 or 0x15 depending on SDO
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- · Copyright (C) 2019 dvanced Micro Devices, Inc
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- Copyright (C) 2019 Google LLC See Documentation/ABI/testing/debugfs-wilco-ec for usage
- Copyright (C) 2019 Google LLC See Documentation/ABI/testing/sysfs-
- Copyright (C) 2019 Google LLC Since the EC will never change the backlight level of its own accord, we don't need to implement a brightness\_get() method
- Copyright (C) 2019 Google LLC Sysfs properties to view and modify EC- controlled features on Wilco devices. The entries will appear under /sys/bus/platform/devices/GOOG000C:00/
- Copyright (C) 2019 Google LLC The Wilco Embedded Controller can create custom events that are not handled as standard ACPI objects. These events can contain information about changes in EC con
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  child will be in a loop writing to FP registe
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- Copyright (C) 2019 IBM Corporation Eddie James
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- Copyright (C) 2019 NXP Author: Daniel Baluta
- Copyright (C) 2019 NXP Author: Daniel Baluta Implementation of the DSP IPC

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- Copyright (C) 2020 Google LLC This driver provides the ability to view and manage Type C ports through the Chrome OS EC
- Copyright (C) 2020 Google LLC This driver serves as the receiver of cros\_ec PD host events
- Copyright (C) 2020 GRATE-driver project
- Copyright (C) 2020 GRATE-driver project. Based on downstream driver from Acer Inc. Based on NVIDIA Gas Gauge driver for SBS Compliant Batteries
- Copyright (C) 2020 Gustavo Luiz Duarte, IBM Corp. This test starts a transaction and triggers a signal, forcing a pagefault to happen when the kernel signal handling code touches the user signal
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  into mobil
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- Copyright (C) 2016 Linaro Ltd Author: Sumit Semwal From internet archives, the panel for Nexus 7 2nd Gen, 2013 model is a JDI model LT070ME05000, and
  its data she
- Copyright (C) 2016 Linaro Ltd Daniel Lezcano define pr\_fmt(fmt)
- Copyright (C) 2016 Linaro Ltd include include include include include include include linux/of\_
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- (C) Adam Belay. include include include include include include include linux/serial\_core
- · (C) Alex Holden
- (C) and server (S): Setup of client connection:
- (C) Architecture Software Developer's Manual, Volume 3: System Prgramming Guide, revision 12 in Table B-1: MSRs in the Pentium 4 and Intel Xeon Processors, on page B-4 and B-5. fsb\_
- (C) A(W1) C(C) Which would create W1-C-W1 dependencies, even though there is no actual deadlock possible. There are two solutions, using a
  read-recursive acquire on the wor
- (C) Balbir Singh, IBM Corp. 2006
- · (C) Benjamin Reed
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- (C) DENX Software Engineering. Author: Yuri Tikhonov
- (C) DVB-S USB2.0
- (C) IBM Corporation, 2011. Author: Suzuki Poulose Based on ppc64 code reloc\_64.S
- (C) in bits ( movq %r12, %xmm1
- (C) INESC 1998
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- (C) Jay Lan, SGI, 2006
- (C) Jean Tourrilhes
- (C) Jens Axboe 2008
- (C) Jiri Kosina
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- (C) Linus Torvalds 'traps.c' handles hardware exceptions after we have saved some state in 'linux/arch/arm/lib/traps.S'. Mostly a debugging aid, but will probably kill the offending pr
- (C) Linux Foundation 2008-2013 Unified
- (C) Nadia Yvette Chambers, April 2004 include includ
- (C) Neil Horman
- (C) Notes: To disconnect the current associated BSS Command Mode struct disconnect\_parm { u32 rsvd;
- (C) Notes: To enter USB suspend mode Command Mode struct usb\_suspend\_parm { u32 action; 1: sleep, 0:resume
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- Copyright © International Business Machines Corp., 2009 DESCRIPTION GCC atomic builtin wrappers Builtins.html
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- Copyright (C) message. 0.0.17 Implement Mic and Line in Capture. 0.0.18 Add support for mute control on SB Live 24bit (cards w/ SPI DAC) This code was
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- Copyright (C) message. 0.0.19 Added I2C and SPI registers. Filled in interrupt enable. 0.0.20 Added GPIO info for SB Live 24bit. 0.0.21 Implement support for Line-in capture o
- Copyright (C) message. 0.0.19 Finally fix support for SB Live 24 bit. SB0410 and SB0413. The output codec needs resetting, otherwise all output is muted.
   0.0.20 Merge
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  happened so this test just uses many thr
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- · Copyright (C) 2016 Cyril Bur, IBM Corp. Sending one self a signal should always get delivered
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- Copyright (C) 2017 Google LLC Based on Intel IPU4 driver
- Copyright (C) 2017 Gustavo Romero, Breno Leitao, Cyril Bur, IBM Corp. Force FP, VEC and VSX unavailable exception during transaction in all possible scenarios regarding the MSR.FP and MSR.VEC st

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  are zero (i.e. just after load\_fp and loa
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- · Copyright (C) 2017 IBM Corporation. This uses code from arch/sparc/kernel/nmi.c and kernel/watchdog.c
- · Copyright (C) 2017 IBM Corp. undef TRACE\_SYSTEM define TRACE\_SYSTEM ocxl
- Copyright (C) 2017 Impinj, Inc Author: Andrey Smirnov Based on the code of analogus driver:
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- Copyright (C) 2018 DEVIALET
- Copyright (C) 2018 Google Inc. Author: Eric Dumazet (edumazet@google.com) Reference program demonstrating tcp mmap() usage, and SO\_RCVLOWAT hints for receiver
- Copyright (C) 2018 Google Inc. Author: Soheil Hassas Yeganeh (soheil@google.com) Simple example on how to use TCP\_INQ and TCP\_CM\_INQ
- Copyright (C) 2016,2018 Google, Inc This driver uses the cros-ec
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  using the input framework https://my.syncplic
- Copyright (C) 2018 Google LLC The corresponding platform device is typically registered in drivers/platform/chrome/wilco\_ec/core.c
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- Copyright (C) 2014,2018 Google LLC. Tomasz Figa
- Copyright (C) 2018 Google LLC. Tomasz Figa Based on s5p-mfc driver by Samsung Electronics Co., Ltd
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  -Added memcmp, strchr, str
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- Copyright (C) 2019 ARM Limited Try to mangle the ucontext from inside a signal handler, mangling the DAIF bits in an illegal manner: this attempt must be spotted by Kernel and the test cas
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- (C) Adam Belay, include include include include include include include
- (C) Alex Holden
- (C) and server (S): Setup of client connection:
- (C) Architecture Software Developer's Manual, Volume 3: System Prgramming Guide, revision 12 in Table B-1: MSRs in the Pentium 4 and Intel Xeon Processors, on page B-4 and B-5. fsb\_
- (C) A(W1) C(C) Which would create W1-C-W1 dependencies, even though there is no actual deadlock possible. There are two solutions, using a read-recursive acquire on the wor
- (C) Balbir Singh, IBM Corp. 2006
- (C) Benjamin Reed
- (C) bits in a HPT entry can be set by hardware at any time up until the HPTE is invalidated and the TLB invalidation sequence has completed. This means that when removing a HPTE
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- (C) Nadia Yvette Chambers, April 2004 include include include include include include include
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- (C) Notes: To disconnect the current associated BSS Command Mode struct disconnect\_parm { u32 rsvd;
- (C) Notes: To enter USB suspend mode Command Mode struct usb\_suspend\_parm { u32 action; 1: sleep, 0:resume
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- Copyright (C) unasserted) AN = 0 (Audio data) P = 0 (Consumer) emu-spdif\_bits 0 = emu-spdif\_bits 1 = emu-spdif\_bits 2 = SPCS\_CLKACCY\_1000PPM
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- (C) 8::codeunit\_t codeunit\_t;
- (C) 8::validate(s,e); }
- (C) AFCET- % Gauthier-Villars % Norman Buckle, UQAH (nb; many additions) % Michael Ferguson, INRS-Telecommunications (mjf) June 1988 % Justi
- $\bullet \quad (C) \ and \ (B) \ win.gBrowser.removeTab(tab\_C); \ win.gBrowser.removeTab(tab\_B); \\$
- (C) and Flip (F) bits are set in the CVO byte. TEST\_F(RtpSenderVideoTest, SendVideoWithCameraAndFlipCVO) {
- (C) below are standard double-angle formulae. (D) is a lesser known result and is derived below:
- (C)eBands j+1 -m-eBands j ) (alloc\_trim-5-LM) (end-j-1) (1(LM+BITRES))6; Giving less resol
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- (C) finishes. Store job begins. storeDone() finishes. Storing of A complete. Storing of C complete. We're done! Call onStoreCompleted. store(B) finishes... uh oh. In oth
- · (C) from above:
- (C) fx5800p. Available under CC-BY-SA. No warranty. lightweightThemes.recommended-4.name=Pastel Gradient
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- (C) int mNum; void act(); };
- (C) is false, return false. 2\. If C has a BoundTargetFunction internal slot, then a. Let BC be the value of C's BoundTargetFunction internal slot. b. Return InstanceofOperato
- (C) is false, return false. features: Symbol.hasInstance \---
- (C) is false, throw a TypeError exception. \---
- (C) is false, throw a TypeError exception. features: Array.prototype.flatMap \---
- (C) is false, throw a TypeError exception. features: Array.prototype.flatten \---
- (C) is false, throw a TypeError exception. features: Symbol.hasInstance \---
- (C) is false, throw a TypeError exception. features: Symbol.species \---
- $\bullet \quad \text{(C) is false, throw a TypeError exception.} \dots \text{includes: testTypedArray.} \text{js features: TypedArray}\\$
- (C) is not Object, throw a TypeError exception. 2\. Let instOfHandler be GetMethod(C,@@hasInstance). 3\. ReturnIfAbrupt(instOfHandler). 4\. If instOfHandler is not undefined, then a. R
- (C) is not Object, throw a TypeError exception. 2\. Let instOfHandler be GetMethod(C,@@hasInstance). 3\. ReturnIfAbrupt(instOfHandler). features: Symbol.hasInstance \---
- (C) is not Object, throw a TypeError exception. 3\. Let promiseCapability be ? NewPromiseCapability(C). ... features: Symbol.species
- (C) is not Object, throw a TypeError exception. 6\. Let S be Get(C, @@species). 7\. ReturnIfAbrupt(S). 8\. If S is either undefined or null, return defaultConstructor. 9\. If IsConstructor
- (C) is not Object, throw a TypeError exception. 6\. Let S be Get(C, @@species). 7\. ReturnIfAbrupt(S). 8\. If S is either undefined or null, return defaultConstructor. features: Symbol.split
- (C) is not Object, throw a TypeError exception. 6\. Let S be Get(C, @@species). 7\. ReturnlfAbrupt(S). features: Symbol.split, Symbol.species \---
- (C) is not Object, throw a TypeError exception. ... features: Symbol
- (C) is not Object, throw a TypeError exception. features: Symbol \---
- (C) is not Object, throw a TypeError exception. features: Symbol.split \---
- (C) is not Object, throw a TypeError exception. ... includes: testTypedArray.js features: Symbol, TypedArray
- $\bullet \quad \text{(C) is Object, then a. Let C be ? Get(C, @@species). b. If C is null, let C be undefined. ..}\\$
- (C) is Object, then a. Let C be ? Get(C, @@species). b. If C is null, let C be undefined. ... 10\. Return ? Construct(C, « length »). features: Symbol.species
- (C) is Object, then a. Let C be ? Get(C, @@species). b. If C is null, let C be undefined. 8\. If C is undefined, return ? ArrayCreate(length)
- (C) is Object, then a. Let C be ? Get(C, @@species). b. If C is null, let C be undefined. 8\. If C is undefined, return ? ArrayCreate(length). features: Symbol.species \---
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- (C) is true, then a. Let A be ? Construct(C, « len »). ..
- (C) is true, then a. Let A be Construct(C, «len»). ..
- (C) is true, then a. Let A be Construct(C, «len»). ... 11\. Return A
- (C) is true, then a. Let A be Construct(C, «len»). 13\. Else, a. Let A be ArrayCreate(len). ... 19\. Return A
- (C) is true, then a. Let A be Construct(C, «len»). 13\. Else, b. Let A be ArrayCreate(len). 14\. ReturnIfAbrupt(A). 15\. Let k be 0. 16\. Repeat, while k len a. Let Pk be ToString(k)
- (C) is true, then a. Let A be Construct(C, «len»). 5\. Else, b. Let A be ArrayCreate(len). ... 11\. Return A
- (C) is true, then a. Let A be Construct(C, «len»). 5\. Else, b. Let A be ArrayCreate(len). 6\. ReturnIfAbrupt(A). ..
- (C) is true, then a. Let thisRealm be the current Realm Record. b. Let realmC be ? GetFunctionRealm(C). c. If thisRealm and realmC are not the same Realm Record, then i
- (C) is true, then i. Let A be ? Construct(C). ..
- (C) is true, then i. Let A be Construct(C). b. Else, i. Let A be ArrayCreate(0). c. ReturnlfAbrupt(A). features: Symbol.iterator \---
- (C) meth = getattr(C, method\_name)
- (C) min = Cmin max = Cmax if n 0.0 C = I + (((C-I) x I)/(I min)) if x 1.0 C = I + (((C-I) x (1-I))/(max I)) return C se
- (C) min = Cmin max = Cmax if n 0.0 C = I + (((C I) x I) / (I min)) if x 1.0 C = I + (((C I) x (1 I)) (max I)) return C set\_lum (C, I):
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- Copyright (C) string. uprv\_memset(dest + sizeof(header), 0, headerSize (int32\_t)sizeof(header)); dest += headerSize; capacity -= headerSize; } else {
- Copyright (C) symbol, for example. The -1 is a bug fix from Rob Laveaux. It keeps an overflow from happening if there is no ';'. There are actually 2 ways to exit this loop -
- Copyright (C) symbol let request1 = new RESTRequest(server.baseURI +
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- (C) to a UErrorCode first test if(U\_FAILURE(errorCode)) { return immediately; } so that in a chain of such functions the first one that sets an error code causes the following ones to not per
- (C) with no optimizations should not display any opts. yield checkFrame(3);

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- (C) self.assertEqual(len(h), orig\_len 1)

- (C) self.assertEqual(len(h), orig\_len 3)
- (C) self.assertln(('self.assertln(('c', 'method', C), attrs, 'missing plain method') self.assertln(('p', 'property', A), attrs, 'missing property') self.assertln(('m'
- (C) self.assertIsSub
- (C) self.assertTrue(issub
- (C) Set-Cookie: keebler=
- (C) Set-Cookie: oreo=doublestuff; Path=/
- (C) should pass silently self.assertRaises(RuntimeError, C.register, A) cycles not allowed C.register(B) ok
- (C) True, (D) False
- (C) with self.assertRaisesRegex(TypeError, asdict(int)
- (C) with self.assertRaisesRegex(TypeError, astuple(int)
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- $\bullet \quad \text{(C) JOIN. } \{X = sqlite3JoinType(pParse,\&A;,\&B;,\&C;); X-overwrites-A\}$
- (C) KiB in size. mxCache = mxCache -1024; }else{ mxCache = mxCache pgsz; } mxCache = MIN(mxCache, SQLITE\_MAX\_PMASZ); pSorter-mxPmaSize = MAX(pSo
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- (C) ruleid ==
- (C) sortorder(Z). A = parserAddExprldListTerm(pParse, 0, &Y;, C, Z); A-overwrites-Y }
- (C) sortorder(Z). A = parserAddExprldListTerm(pParse, A, &Y;, C, Z); }
- (C) (strlen(C) char sqlite3ColumnType(Column ,char ); define sqlite3StrNICmp sqlite3\_strnicmp
- (C) tablename = (D) idcolumn = \$idcolumn (E) parentcolumn = \$parentcolumn
- (C) The content of every column in the row
- (C) Unique index that do use OE\_Replace by default. The ordering of (2) and (3) is accomplished by making sure the linked list of indexes attached to a table
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SLA0048 Rev4/March 2018

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# (busybox:1.31.1, Bzip2:1.0.8)

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