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- (C) add (1, endif
- (C) add (1, fputc (C, p)) else
- (C) __btowc (C) else
- Copyright (C) sc_man_check_x_Copyright (C) @status=0; \ cd \$(srcdir) && wc -cl man.x | head -n-1 \ | awk '\$\$1 = 20 || \$\$2 = 1000 {print \$\$3}' \ | xargs grep -L 'Copyright . Free Softwa
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- (C) self.assertEqual(ids self.assertEqual(ids 0 , GObject.signal_lookup('my- signal', C))
- (C) self.assertEqual(len(ids), 1) Note canonicalized names self.assertEqual(GObject.signal_name(ids 0), 'my-signal') There is no signal 0 in GObject self.a

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- Copyright (C) 2006-2012 Patrick McHardy Based on the old ipv4-only ipt_ULOG.c:
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- Copyright (C) 2007 Red Hat Inc Based upon linux/drivers/ide/pci/cmd640.c Version 1.02 Sep 01, 1996
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- Copyright (C) 2017-2019 Jacopo Mondi jacopo
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- Copyright (C) 2017-2018 NXP Author: Dong Aisheng
- Copyright (C) 2017-2018 NXP Dong Aisheng
- Copyright (C) 2017-2018 NXP Dong Aisheng Implementation of the SCU based Power Domains
- Copyright (C) 2017-2020 NXP Header file containing the
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- Copyright (C) 2017 Advanced Micro Devices, Inc. This file is modified from rt288 machine driver
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- Copyright (C) 2017 Analog Devices Inc. include include include include include include in
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- Copyright (C) 2017 Anshuman Khandual, IBM Corp. Works on architectures which support 128TB virtual address range and beyond
- Copyright (C) 2017 Author: Dong Aisheng
- Copyright (C) 2017 Author: Dong Aisheng File containing client-side RPC functions for the MISC service. These function are ported to clients that communicate to the SC
- Copyright (C) 2017 Ben Whitten
- Copyright (C) 2017 by Helge Deller possible future enhancements:
- Copyright (C) 2017 Cadence Design Systems Inc. include include include include include

- Copyright (C) 2017 Canonical Ltd. TODO If a task uses change_hat it currently does not return to the old cred or task context but instead creates a new one. Ideally the task should return
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- Copyright (C) 2017 Cirrus Logic, Inc. Authors: Li Xu
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- Copyright (C) 2017 Google, Inc This driver uses the memory mapper cros-ec
- Copyright (C) 2017 Google LLC Based on Intel IPU4 driver
- Copyright (C) 2017 Gustavo Romero, Breno Leitao, Cyril Bur, IBM Corp. Force FP, VEC and VSX unavailable exception during transaction in all possible scenarios regarding the MSR.FP and MSR.VEC st
- Copyright (C) 2017 Gustavo Romero, IBM Corp. Check if thread endianness is flipped inadvertently to BE on trap caught in TM whilst MSR.FP and MSR.VEC are zero (i.e. just after load_fp and loa
- Copyright (C) 2017 Hannes Reinecke, SUSE Linux GmbH Based on the original DAC960 driver
- Copyright (C) 2017-2018 Hannes Reinecke, SUSE Linux GmbH Based on the original DAC960 driver, which has
- Copyright (C) 2017 Header file containing the
- Copyright (C) 2017 IBM Corp. include include include include
- Copyright (C) 2017 IBM Corp. include include include include include include include include asm/r
- Copyright (C) 2017 IBM Corp. include include include include include include include include include asm/
- Copyright (C) 2017,2019 IBM Corp. include include
- Copyright (C) 2017 IBM Corp. include
- Copyright (C) 2017 IBM Corporation cpufeatures is the new way to discover CPU features with /cpus/features devicetree. This supersedes PVR based discovery (
- Copyright (C) 2017 IBM Corporation. This uses code from arch/sparc/kernel/nmi.c and kernel/watchdog.c
- Copyright (C) 2017 IBM Corp. undef TRACE_SYSTEM define TRACE_SYSTEM ocxl
- Copyright (C) 2017 Impinj, Inc Author: Andrey Smirnov Based on the code of analogous driver:
- Copyright (C) 2017 John Sperbeck Test that an access to a mapped but inaccessible area causes a SEGV and reports si_code == SEGV_ACCERR
- Copyright (C) 2017 Keith Packard include include
- Copyright (C) 2017 Linaro Ltd. Author: Baoyou Xie
- Copyright (C) 2017 Manivannan Sadhasivam IIO driver for VL6180 (7-bit I2C slave address 0x29)
- Copyright (C) 2017 Maxime Roussin-B expanded by Silvan Murer
- Copyright (C) 2017 Michael Neuling, IBM Corp. Original: Breno Leitao & Gustavo Bueno Romero Edited: Michael Neuling Force VMX unavailable
- Copyright (C) 2017 Nicholas Piggin, IBM Corporation
- Copyright (C) 2017 Noralf Tr The driver supports:
- Copyright (C) 2017 Nuvoton Technology Corp
- Copyright (C) 2017 Nuvoton Technology Corp. Author: David Lin Co-author: John Hsu Co-author: Seven Li
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<https://www.idt.com/document/dst/zopt2201-datasheet>
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- Copyright (C) 2018-2019 NXP Based on caamalg.c crypto API driver
- Copyright (C) 2018-2020 NXP include include include
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- Copyright (C) 2018 Andrej Shadura
- Copyright (C) 2018 Arcx Inc
- Copyright (C) 2018 Arm Limited Author: Dave Martin include include include include include as
- Copyright (C) 2018 Bharata B Rao, IBM Corp
- Copyright (C) 2018 Breno Leita, Gustavo Romero, IBM Corp. A test case that creates a signal and starts a suspended transaction inside the signal handler
- Copyright (C) 2018 Breno Leita, Gustavo Romero, IBM Corp. This test raises a SIGUSR1 signal, and toggle the MSR TS fields at the signal handler. With MSR TS being set, the kernel will forc
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- Copyright (C) 2018 Cirrus Logic, Inc. Author: James Schulman
- Copyright (C) 2018 David Lechner Based on mi0283qt.c:
- Copyright (C) 2018 DEVIALET
- Copyright (C) 2018 Google Inc. Author: Eric Dumazet (edumazet@google.com) Reference program demonstrating tcp mmap() usage, and SO_RCVLOWAT hints for receiver
- Copyright (C) 2018 Google Inc. Author: Soheil Hassas Yeganeh (soheil@google.com) Simple example on how to use TCP_INQ and TCP_CM_INQ
- Copyright (C) 2016,2018 Google, Inc This driver uses the cros-ec
- Copyright (C) 2018 Google LLC. Author: Tomasz Figa Author: Yong Zhi
- Copyright (C) 2018 Google LLC. Author: Tuukka Toivonen Author: Sakari Ailus Author: Samu Onkalo Author: To
- Copyright (C) 2018 Google LLC. Driver for Semtech's SX9310/SX9311 capacitive proximity/button solution. Based on SX9500 driver and Semtech driver using the input framework <https://my.syncplic>
- Copyright (C) 2018 Google LLC The corresponding platform device is typically registered in drivers/platform/chrome/wilco_ec/core.c
- Copyright (C) 2018 Google LLC The Wilco EC is similar to a typical ChromeOS embedded controller. It uses the same MEC based low-level communication and a similar protocol, but with some import
- Copyright (C) 2018 Google LLC This is the entry point for the drivers that control the Wilco EC
- Copyright (C) 2014,2018 Google LLC. Tomasz Figa
- Copyright (C) 2018 Google LLC. Tomasz Figa Based on s5p-mfc driver by Samsung Electronics Co., Ltd
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- Copyright (C) 2018 IBM Corporation Author: Anshuman Khandual
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- Copyright (C) 2018 Intel Corporation Author: Gaurav K Singh Manasi Navare
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- Copyright (C) 2018 Lubomir Rintel include include include include include include
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- Copyright (C) 2018 Michael Ellerman, IBM Corp. Test that an out-of-bounds branch to counter behaves as expected
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- Copyright (C) 2018 SUSE Linux. Author: Coly Li include include
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- Copyright (C) 2019 Analog Devices Inc. include include include include include include include include linux/interrupt
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- Copyright (C) 2019 Annaliese McDermond Author: Annaliese McDermond
- Copyright (C) 2019 ARM Limited 32 Bit compat layer by Stefani Seibold sponsored by Rohde & Schwarz GmbH & Co. KG Munich/Germany
- Copyright (C) 2019 Arm Limited Author: Andrew Murray include include include include
- Copyright (C) 2019 Arm Limited Authors: Mark Rutland Amit Daniel Kachhap
- Copyright (C) 2019 ARM Ltd. Generic implementation of update_vsyscall and update_vsyscall_tz
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- Copyright (C) 2019 Collabora Ltd. include include include include include include include include
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- Copyright (C) 2019 dvanced Micro Devices, Inc
- Copyright (C) 2019 Finn Thain derived in part from:
- Copyright (C) 2019 Google, Inc. ChromeOS Embedded Controller codec driver
- Copyright (C) 2019 Google, LLC
- Copyright (C) 2019-2020 Google LLC. include include
- Copyright (C) 2019 Google LLC See Documentation/ABI/testing/debugfs-wilco-ec for usage
- Copyright (C) 2019 Google LLC See Documentation/ABI/testing/sysfs-
- Copyright (C) 2019 Google LLC Since the EC will never change the backlight level of its own accord, we don't need to implement a brightness_get() method
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- Copyright (C) 2019 Google LLC The Wilco Embedded Controller can create custom events that are not handled as standard ACPI objects. These events can contain information about changes in EC con

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- Copyright (C) 2019 Gustavo Romero, Michael Neuling, IBM Corp. This test will spawn two processes. Both will be attached to the same CPU (CPU 0). The child will be in a loop writing to FP registe
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- Copyright (C) 2019 IBM Corporation Eddie James
- Copyright (C) 2019 Inspur Corp
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- Copyright (C) 2019 Linaro, Ltd, Rob Herring
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- Copyright (C) 2019 Linaro, Ltd, Rob Herring Register definitions based on mali_midg_regmap.h
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- Copyright (C) 2019 Nick Piggin, Gautham R. Shenoy, Aneesh Kumar K.V, IBM Corp
- Copyright (C) 2019 NXP Author: Daniel Baluta
- Copyright (C) 2019 NXP Author: Daniel Baluta Implementation of the DSP IPC
- Copyright (C) 2019 NXP Clock driver for LS1028A Display output
- Copyright (C) 2019 NXP Header file for the DSP IPC implementation
- Copyright (C) 2019 NXP Implementation of the SCU IRQ functions using MU
- Copyright (C) 2019 NXP include include
- Copyright (C) 2019 NXP Peng Fan
- Copyright (C) 2019 NXP. Scaling algorithms were contributed by Dzung Hoang
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- Copyright (C) 2019 NXP Semiconductors This is an umbrella module for all network switches that are register-compatible with Ocelot and that perform I/O to their host CPU through an NPI (Node P
- Copyright (C) 2019 Ondrej Zary Original driver by Rickard E. Faith, faith@cs.unc.edu
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- Copyright (C) 2020 Google LLC. Author: Sean O'Brien
- Copyright (C) 2020 Google LLC. include
- Copyright (C) 2020 Google LLC. Julius Werner Based on mtk_wdt.c
- Copyright (C) 2020 Google LLC This driver provides the ability to view and manage Type C ports through the Chrome OS EC
- Copyright (C) 2020 Google LLC This driver serves as the receiver of cros_ec PD host events
- Copyright (C) 2020 Gustavo Luiz Duarte, IBM Corp. This test starts a transaction and triggers a signal, forcing a pagefault to happen when the kernel signal handling code touches the user signal
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- Copyright (C) 2020 NXP Semiconductors include
- Copyright (C) 2020 Palmer Dabbelt
- Copyright (C) 2020 Puresoftware Ltd. FlexSPI is a flexible SPI host controller which supports two SPI channels and up to 4 external devices. Each channel supports Single/Dual/Quad/Octal mode
- Copyright (C) 2020 Puresoftware Ltd. This isn't a full driver; it just provides an alternate IRQ handler to deal with an errata and provide ACPI wrapper. Everything else is just using the bog
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- Copyright (C) 2020 Sandipan Das, IBM Corp
- Copyright (C) 2020 Sandipan Das, IBM Corp. Test if applying execute protection on pages using memory protection keys works as expected
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- Copyright (C) 1990,1999-2001 by Silicon Graphics, Inc
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- Copyright (C) 1991-1994 Linus Torvalds Modified by Fred N. van Kempen, 01/29/93, to add line disciplines which can be dynamically activated and de-activated by the line disci
- Copyright (C) 1991-1994 Linus Torvalds Swap reorganised 29.12.95, Asynchronous swapping added 30.12.95. Stephen Tweedie Removed race in async swapping. 14.4.1996. Bruno H
- Copyright (C) 1991-1994 Linus Torvalds Swap reorganised 29.12.95, Stephen Tweedie
- Copyright (C) 1991-1994 Linus Torvalds Swap reorganised 29.12.95, Stephen Tweedie. kswapd added: 7.1.96 sct Removed kswapd_ctl limits, and swap out as many pages as neede
- Copyright (C) 1991-1994 Linus Torvalds Swap reorganised 29.12.95, Stephen Tweedie Support of BIGMEM added by Gerhard Wichert, Siemens AG, July 1999 Reshaped it to be a zoned
- Copyright (C) 1991-1994 Linus Torvalds zoned VM statistics
- Copyright (C) 1991-1992,1995,1999-2000 Linus Torvalds This file contains date handling
- Copyright (C) 1991-1992,1995,1999-2000 Linus Torvalds This file contains the clocksource time handling. 1997-09-10 Updated NTP code according to technical memorandum Jan '96
- Copyright (C) 1991-1992,1995 Linus Torvalds Adapted for PowerPC (PReP) by Gary Thomas Modified by Cort Dougan (cort@cs.nmt.edu). Copied and modified from arch/i386/kernel/time.c
- Copyright (C) 1991-1992,1995 Linus Torvalds This file contains the m68k-specific time handling details. Most of the stuff is located in the machine specific files
- Copyright (C) 1991-1992,1996 Linus Torvalds Hacked together by Andi Kleen
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- Copyright (C) 1991-1992 Linus Torvalds 1995 Jay Estabrook A note on using in_atomic() in here: We can't handle console calls from non- schedulable context due to our USB-dependend
- Copyright (C) 1991-1992 Linus Torvalds 1995 Jay Estabrook User definable mapping table and font loading by Eugene G. Crosser
- Copyright (C) 1991-1992 Linus Torvalds 1997-01-28 Modified by Finn Arne Gangstad to make timers scale better
- Copyright (C) 1991-1992,1995 Linus Torvalds 1997-11-02 Modified for POSIX.1b signals by Richard Henderson
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- Copyright (C) 1991-1992 Linus Torvalds 64-bit file support on 64-bit platforms by Jakub Jelinek (jj@sunsite.ms.mff.cuni.cz)
- Copyright (C) 1991-1992 Linus Torvalds Added devfs support. Jan-11-1998, C. Scott Ananian Shared /dev/zero mmappping support, Feb 2000, Kanoj Sarcar
- Copyright (C) 1991-1992 Linus Torvalds Added support for a Unix98-style ptmx device. -- C. Scott Ananian , 14-Jan-1998
- Copyright (C) 1991-1992 Linus Torvalds adfs regular file handling primitives
- Copyright (C) 1991-1992 Linus Torvalds affs symlink handling code
- Copyright (C) 1991-1992 Linus Torvalds Big-endian to little-endian byte- swapping/bitmaps by David S. Miller (davem@caip.rutgers.edu), 1995
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- Copyright (C) 1991-1992 Linus Torvalds changes by Thomas Schoebel-Theuer
- Copyright (C) 1991-1992 Linus Torvalds coh/dir.c
- Copyright (C) 1991-1992 Linus Torvalds coh/file.c
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- Copyright (C) 1991-1992 Linus Torvalds ext2 directory handling functions
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- Copyright (C) 1991-1992 Linus Torvalds ext4 symlink handling code
- Copyright (C) 1991-1992 Linus Torvalds ext/freelists.c
- Copyright (C) 1991-1992 Linus Torvalds GK 2/5/95 - Changed to support mounting root fs via NFS Added initrd & change_root: Werner Almesberger & Hans Lermen, Feb '96 Moan early if gc
- Copyright (C) 1991-1992 Linus Torvalds Goal-directed block allocation by Stephen Tweedie (sct@dcs.ed.ac.uk), 1993, 1998 Big-endian to little-endian byte-swapping/bitmaps by
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- Copyright (C) 2007-2008 Darron Broad March 2007 Fixed some bugs. Added diseqc support. Added corrected signal strength support. August 2007 Sync with legacy version. Some cl
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- Copyright (C) 2007-2009 ST-Ericsson AB ABX500 core access functions. The abx500
- Copyright (C) 2007-2009 ST-Ericsson AB Driver to read out OTP from the AB3100 Mixed-signal circuit Author: Linus Walleij
- Copyright (C) 2007-2009 ST-Ericsson AB Real Time Clock
- Copyright (C) 2007-2009 ST-Ericsson AB RTC clock driver for the AB3100 Analog Baseband Chip Author: Linus Walleij include include linux/ker
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- Copyright (C) 2007-2013 ST-Ericsson DMA driver for COH 901 318 Author: Per Friden
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- Copyright (C) 2007-2010 Paul Mundt include include include include include include linux/gpio
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- Copyright (C) 2009-2010 Nokia Corporation Written by Paul Walmsley Testing and integration fixes by Jouni Högander
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- Copyright (C) 2019 ARM Limited Place a fake sigframe on the stack including a badly sized terminator record: on sigreturn Kernel must spot this attempt and the test case is expected to be
- Copyright (C) 2019 ARM Limited Place a fake sigframe on the stack including a bad record overflowing the __reserved space: on sigreturn Kernel must spot this attempt and the test case is e
- Copyright (C) 2019 ARM Limited Place a fake sigframe on the stack including a BAD Unknown magic record: on sigreturn Kernel must spot this attempt and the test case is expected to be termi
- Copyright (C) 2019 ARM Limited Place a fake sigframe on the stack including an additional FPSIMD record: on sigreturn Kernel must spot this attempt and the test case is expected to be term
- Copyright (C) 2019 ARM Limited Place a fake sigframe on the stack missing the mandatory FPSIMD record: on sigreturn Kernel must spot this attempt and the test case is expected to be termin
- Copyright (C) 2019 ARM Limited Try to mangle the ucontext from inside a signal handler, mangling the DAIF bits in an illegal manner: this attempt must be spotted by Kernel and the test cas
- Copyright (C) 2019 ARM Limited Try to mangle the ucontext from inside a signal handler, toggling the execution state bit: this attempt must be spotted by Kernel and the test case is expect
- Copyright (C) 2019 ARM Limited Try to mangle the ucontext from inside a signal handler, toggling the mode bit to escalate exception level: this attempt must be spotted by Kernel and the te
- Copyright (C) 2019 ARM Limited Utility macro to ease definition of testcases toggling mode EL
- Copyright (C) 2019 ARM Ltd. Author: Anshuman Khandual
- Copyright (C) 2019 Arm Ltd. Based on msm_gem_freedreno.c:
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- Copyright (C) 2019 Facebook define STACK_MAX_LEN 50
- Copyright (C) 2019 Facebook define STACK_MAX_LEN 600 clang will not unroll the loop 600 times. Instead it will unroll it to the amount it deemed appropriate, but the loop will still execute
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- Copyright (C) 2019 Facebook include include include include include include include include include
- Copyright (C) 2019 Facebook include include include include include include include include linux/btf_i
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- Copyright (C) 2019 Facebook include include include include include include include include
- Copyright (C) 2019 Facebook include include include include include include include include inclu
- Copyright (C) 2019 Facebook One challenge of controlling IO resources is the lack of trivially observable cost metric. This is distinguished from CPU and memory where wallclock time and t
- Copyright (C) 2019 Facebook ----- START-EXPECTED-OUTPUT ----- enum e1 { A = 0, B = 1
- Copyright (C) 2019 Facebook ----- START-EXPECTED-OUTPUT ----- struct padded_implicitly { int a; long int b; char c;
- Copyright (C) 2019 Facebook ----- START-EXPECTED-OUTPUT ----- struct s1 {};
- Copyright (C) 2019 Facebook ----- START-EXPECTED-OUTPUT ----- struct S { int S; int U;
- Copyright (C) 2019 Facebook ----- START-EXPECTED-OUTPUT ----- typedef int arr_t 2 ;
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- Copyright (C) 2020 Facebook define _GNU_SOURCE include include include include include
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- Copyright (C) 2020 Facebook define MAX_STACK (512 - 3 32)
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- Copyright (C) 2020 Facebook define STACK_MAX_LEN 50 define SUBPROGS
- Copyright (C) 2020 Facebook define START_CHAR 'a'
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- Copyright (C) 2020 Google LLC. Tests for KVM paravirtual feature disablement
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- (C) 2 - 1) : ((C)
- (C) Adam Belay. include include include include include include include
- (C) Alex Holden
- (C) and server (S): Setup of client connection:
- (C) Architecture Software Developer's Manual, Volume 3: System Programming Guide, revision 12 in Table B-1: MSRs in the Pentium 4 and Intel Xeon Processors, on page B-4 and B-5. fsb_
- (C) A(W1) C(C) Which would create W1-C-W1 dependencies, even though there is no actual deadlock possible. There are two solutions, using a read-recursive acquire on the work
- (C) Balbir Singh, IBM Corp. 2006
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- (C) bits in a HPT entry can be set by hardware at any time up until the HPTE is invalidated and the TLB invalidation sequence has completed. This means that when removing a HPTE
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- Copyright (C) 2004 Pavel Machek Based upon datasheets & sample CPUs kindly provided by AMD. Valuable input gratefully received from Dave Jones, Pavel Machek, Dominik Brodowski, Jac
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- Copyright (C) 2005-2006 Red Hat Inc Alan Cox based upon cs5535.c from AMD as cleaned up and made readable and Linux style by Wolfgang Zuleger w
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- Copyright (C) 2006-2009 VIA Technology, Inc
- Copyright (C) 2006-2012 include include include include include include include include net/addrc
- Copyright (C) 2006-2012 include include include include include include include include linux/export
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- Copyright (C) 2010-2011 Thomas Renninger , Novell Inc. Output format inspired by Len Brown's turbostat tool
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- Copyright (C) 2015 Michael Neuling, IBM Corp. Test the kernel's signal delievery code to ensure that we don't trelaim twice in the kernel signal delivery code. This can happen if we trigger
- Copyright (C) 2015 Michael Neuling, IBM Corp. Test the kernel's signal return code to ensure that it doesn't crash when both the transactional and suspend MSR bits are set in the signal conte
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- Copyright (C) 2014-2015 Pengutronix, Markus Pargmann
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- Copyright (C) 2008,2011,2014-2017 Robert Jarzmik
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- Copyright (C) 2015 Samuel Pitosiet
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- Copyright (C) 2015 Toradex AG Stefan Agner
- Copyright (C) 2015 Verifone Int. Author: Nicolas Saenz Julienne
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- Copyright (C) 2016 Chen-Yu Tsai Based on the Allwinner SDK driver
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- Copyright (C) 2016 Christophe Leroy, CS Systemes d
- Copyright (C) 2016-2017 Cirrus Logic, Inc. Author: Brian Austin
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- Copyright (C) 2016 Cyril Bur, IBM Corp. Sending one self a signal should always get delivered
- Copyright (C) 2016 Cyril Bur, IBM Corp. Syscalls can be performed provided the transactions are suspended. The exec()
- Copyright (C) 2016 Cyril Bur, IBM Corp. Test the kernel's signal frame code
- Copyright (C) 2016 David Lechner Based on iio/ad7923.c:
- Copyright (C) 2016 Elettra-Sincrotrone Trieste S.C.p.A. Author: Alessio Igor Bogani
- Copyright (C) 2012-2014,2016 Ezequiel Garcia
- Copyright (C) 2016 Ezequiel Garcia Based on LED IDE-Disk Activity Trigger
- Copyright (C) 2014,2016-2017 Free Electrons Author: Boris Brezillon
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- Copyright (C) 2016 Google Inc. include include include include include include
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- Copyright (C) 2016-2021 Microsemi Corporation
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- Copyright (C) 2016 Nuvoton Technology Corp. Author: John Hsu
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- Copyright (C) 2016 Qtechnology A/S 2016 Ricardo Ribalda include include include include include include linux
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- Copyright (C) 2017-2019 Jacopo Mondi jacopo
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- Copyright (C) 2017-2018 NXP Author: Dong Aisheng
- Copyright (C) 2017-2021 NXP Dong Aisheng
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- Copyright (C) 2017 Author: Dong Aisheng File containing client-side RPC functions for the MISC service. These function are ported to clients that communicate to the SC
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- Copyright (C) 2017 Cirrus Logic, Inc. Authors: Li Xu
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- Copyright (C) 2017 Dong Aisheng
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- Copyright (C) 2017 Google Inc Provides a simple driver to control the ASPEED LPC snoop
- Copyright (C) 2017 Google, Inc This driver uses the memory mapper cros-ec
- Copyright (C) 2017 Google LLC Based on Intel IPU4 driver
- Copyright (C) 2017 Gustavo Romero, Breno Leitao, Cyril Bur, IBM Corp. Force FP, VEC and VSX unavailable exception during transaction in all possible scenarios regarding the MSR.FP and MSR.VEC st

- Copyright (C) 2017 Gustavo Romero, IBM Corp. Check if thread endianness is flipped inadvertently to BE on trap caught in TM whilst MSR.FP and MSR.VEC are zero (i.e. just after load_fp and loa
- Copyright (C) 2017 Hannes Reinecke, SUSE Linux GmbH Based on the original DAC960 driver
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- Copyright (C) 2017 IBM Corp. include include include include include include include include
- Copyright (C) 2017,2019 IBM Corp. include include
- Copyright (C) 2017 IBM Corp. include
- Copyright (C) 2017 IBM Corporation cpufeatures is the new way to discover CPU features with /cpus/features devicetree. This supersedes PVR based discovery (
- Copyright (C) 2017 IBM Corporation. This uses code from arch/sparc/kernel/nmi.c and kernel/watchdog.c
- Copyright (C) 2017 IBM Corp. undef TRACE_SYSTEM define TRACE_SYSTEM ocxl
- Copyright (C) 2017 Impinj, Inc Author: Andrey Smirnov Based on the code of analogous driver:
- Copyright (C) 2017 Joel Stanley
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- Copyright (C) 2017 Keith Packard include include
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- Copyright (C) 2017,2020 Nuvoton Technology Crop
- Copyright (C) 2017 Nuvoton Technology Crop. Author: David Lin Co-author: John Hsu Co-author: Seven Li
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<https://www.idt.com/document/dst/zopt2201-datasheet>
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- Copyright (C) 2017 Tim Harvey
- Copyright (C) 2017 Valve Corporation
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- Copyright (C) 2018 Bharata B Rao, IBM Corp
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- Copyright (C) 2018 Breno Leitao, Gustavo Romero, IBM Corp. This test raises a SIGUSR1 signal, and toggle the MSR TS fields at the signal handler. With MSR TS being set, the kernel will forc
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- Copyright (C) 2018 David Lechner Based on mi0283qt.c:
- Copyright (C) 2018 DEVIALET
- Copyright (C) 2018 Google Inc. Author: Eric Dumazet (edumazet@google.com) Reference program demonstrating tcp mmap() usage, and SO_RCVLOWAT hints for receiver
- Copyright (C) 2018 Google Inc. Author: Soheil Hassas Yeganeh (soheil@google.com) Simple example on how to use TCP_INQ and TCP_CM_INQ
- Copyright (C) 2016,2018 Google, Inc This driver uses the cros-ec
- Copyright (C) 2018,2020 Google LLC Author: Eric Biggers
- Copyright (C) 2018 Google LLC. Author: Tomasz Figa Author: Yong Zhi
- Copyright (C) 2018 Google LLC. Author: Tuukka Toivonen Author: Sakari Ailus Author: Samu Onkalo Author: To
- Copyright (C) 2018 Google LLC. Driver for Semtech's SX9310/SX9311 capacitive proximity/button solution. Based on SX9500 driver and Semtech driver using the input framework <https://my.syncplic>
- Copyright (C) 2018 Google LLC The corresponding platform device is typically registered in drivers/platform/chrome/wilco_ec/core.c
- Copyright (C) 2018 Google LLC The Wilco EC is similar to a typical ChromeOS embedded controller. It uses the same MEC based low-level communication and a similar protocol, but with some import
- Copyright (C) 2018 Google LLC This is the entry point for the drivers that control the Wilco EC
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- Copyright (C) 2018 Google LLC. Tomasz Figa Based on s5p-mfc driver by Samsung Electronics Co., Ltd
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- Copyright (C) 2018 IBM Corporation Author: Anshuman Khandual
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- Copyright (C) 2018 Lubomir Rintel include include include include include include
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- Copyright (C) 2018 Marty E. Plummer
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- Copyright (C) 2018 Michael Ellerman, IBM Corp. Test that an out-of-bounds branch to counter behaves as expected
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- Copyright (C) 2018 SUSE Linux. Author: Coly Li include include
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- Copyright (C) 2019 ARM Ltd. Generic implementation of update_vsyscall and update_vsyscall_tz
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- Copyright (C) 2019 Dan Robertson I2C address is either 0x14 or 0x15 depending on SDO
- Copyright (C) 2019 Dan Robertson TODO:
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- Copyright (C) 2019 Google, LLC
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- Copyright (C) 2019 Google LLC See Documentation/ABI/testing/sysfs-
- Copyright (C) 2019 Google LLC Since the EC will never change the backlight level of its own accord, we don't need to implement a brightness_get() method
- Copyright (C) 2019 Google LLC Sysfs properties to view and modify EC- controlled features on Wilco devices. The entries will appear under /sys/bus/platform/devices/GOOG000C:00/
- Copyright (C) 2019 Google LLC The Wilco Embedded Controller can create custom events that are not handled as standard ACPI objects. These events can contain information about changes in EC con
- Copyright (C) 2019 Google LLC The Wilco Embedded Controller is able to send telemetry data which is useful for enterprise applications. A daemon running on the OS sends a command to the EC via
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- Copyright (C) 2020 Google LLC. Julius Werner Based on mtk_wdt.c
- Copyright (C) 2020 Google LLC This driver provides the ability to view and manage Type C ports through the Chrome OS EC
- Copyright (C) 2020 Google LLC This driver serves as the receiver of cros_ec PD host events
- Copyright (C) 2020 GRATE-driver project
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- Copyright (C) 2020 Gustavo Luiz Duarte, IBM Corp. This test starts a transaction and triggers a signal, forcing a pagefault to happen when the kernel signal handling code touches the user signal
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- Copyright (C) 2019 ARM Limited Try to mangle the ucontext from inside a signal handler, mangling the DAIF bits in an illegal manner: this attempt must be spotted by Kernel and the test cas
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- Copyright (C) 2019 Facebook define STACK_MAX_LEN 50
- Copyright (C) 2019 Facebook define STACK_MAX_LEN 600 clang will not unroll the loop 600 times. Instead it will unroll it to the amount it deemed appropriate, but the loop will still execute
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- Copyright (C) 2019-2021 Facebook include include include include include include include include include
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- Copyright (C) 2019 Facebook One challenge of controlling IO resources is the lack of trivially observable cost metric. This is distinguished from CPU and memory where wallclock time and t
- Copyright (C) 2019 Facebook ----- START-EXPECTED-OUTPUT ----- enum e1 { A = 0, B = 1
- Copyright (C) 2019 Facebook ----- START-EXPECTED-OUTPUT ----- struct padded_implicitly { int a; long int b; char c;
- Copyright (C) 2019 Facebook ----- START-EXPECTED-OUTPUT ----- struct s1 {};
- Copyright (C) 2019 Facebook ----- START-EXPECTED-OUTPUT ----- struct S { int S; int U;
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- (C) Adam Belay. include include include include include include include linux/serial_core
- (C) Alex Holden
- (C) and server (S): Setup of client connection:
- (C) Architecture Software Developer's Manual, Volume 3: System Prgramming Guide, revision 12 in Table B-1: MSRs in the Pentium 4 and Intel Xeon Processors, on page B-4 and B-5. fsb_
- (C) A(W1) C(C) Which would create W1-C-W1 dependencies, even though there is no actual deadlock possible. There are two solutions, using a read-recursive acquire on the wor
- (C) Balbir Singh, IBM Corp. 2006
- (C) Benjamin Reed
- (C) bits in a HPT entry can be set by hardware at any time up until the HPTE is invalidated and the TLB invalidation sequence has completed. This means that when removing a HPTE
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- Copyright (C) 2006-2009 VIA Technology, Inc
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- Copyright (C) 2006-2012 include include include include include include include include linux/export
- Copyright (C) 2006-2012 Patrick McHardy Based on the old ipv4-only ipt_ULOG.c:
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- Copyright (C) 2007 Pavel Emelyanov , OpenVZ, SWsoft Inc
- Copyright (C) 2007 Red Hat Inc Based upon linux/drivers/ide/pci/cmd640.c Version 1.02 Sep 01, 1996
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- Copyright (C) 2010-2011 Thomas Renninger , Novell Inc. Ideas taken over from the perf userspace tool (included in the Linux kernel git repo): subcommand builtins and param parsin
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- Copyright (C) 2017 Google, Inc This driver uses the memory mapper cros-ec
- Copyright (C) 2017 Google LLC Based on Intel IPU4 driver
- Copyright (C) 2017 Gustavo Romero, Breno Leitao, Cyril Bur, IBM Corp. Force FP, VEC and VSX unavailable exception during transaction in all possible scenarios regarding the MSR.FP and MSR.VEC st
- Copyright (C) 2017 Gustavo Romero, IBM Corp. Check if thread endianness is flipped inadvertently to BE on trap caught in TM whilst MSR.FP and MSR.VEC are zero (i.e. just after load_fp and loa
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- Copyright (C) 2017 IBM Corp. undef TRACE_SYSTEM define TRACE_SYSTEM ocxl
- Copyright (C) 2017 Impinj, Inc Author: Andrey Smirnov Based on the code of analogus driver:
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- Copyright (C) 2018 DEVIALET
- Copyright (C) 2018 Google Inc. Author: Eric Dumazet (edumazet@google.com) Reference program demonstrating tcp mmap() usage, and SO_RCVLOWAT hints for receiver
- Copyright (C) 2018 Google Inc. Author: Soheil Hassas Yeganeh (soheil@google.com) Simple example on how to use TCP_INQ and TCP_CM_INQ
- Copyright (C) 2016,2018 Google, Inc This driver uses the cros-ec
- Copyright (C) 2018 Google LLC Author: Eric Biggers
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- Copyright (C) 2018 Google LLC. Author: Tuukka Toivonen Author: Sakari Ailus Author: Samu Onkalo Author: To
- Copyright (C) 2018 Google LLC. Driver for Semtech's SX9310/SX9311 capacitive proximity/button solution. Based on SX9500 driver and Semtech driver using the input framework <https://my.syncpllc>
- Copyright (C) 2018 Google LLC The corresponding platform device is typically registered in drivers/platform/chrome/wilco_ec/core.c
- Copyright (C) 2018 Google LLC The Wilco EC is similar to a typical ChromeOS embedded controller. It uses the same MEC based low-level communication and a similar protocol, but with some import
- Copyright (C) 2018 Google LLC This is the entry point for the drivers that control the Wilco EC
- Copyright (C) 2014,2018 Google LLC. Tomasz Figa
- Copyright (C) 2018 Google LLC. Tomasz Figa Based on s5p-mfc driver by Samsung Electronics Co., Ltd
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- Copyright (C) 2018 IBM Corporation Author: Anshuman Khandual
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- Copyright (C) 2019 Google LLC Since the EC will never change the backlight level of its own accord, we don't need to implement a brightness_get() method
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- Copyright (C) 2019 NXP Author: Daniel Baluta Implementation of the DSP IPC
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- Copyright (C) 2020 Google LLC This driver provides the ability to view and manage Type C ports through the Chrome OS EC
- Copyright (C) 2020 Google LLC This driver serves as the receiver of cros_ec PD host events
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- Copyright (C) 1991-1994 Linus Torvalds Swap reorganised 29.12.95, Stephen Tweedie
- Copyright (C) 1991-1994 Linus Torvalds Swap reorganised 29.12.95, Stephen Tweedie. kswapd added: 7.1.96 sct Removed kswapd_ctl limits, and swap out as many pages as neede
- Copyright (C) 1991-1994 Linus Torvalds Swap reorganised 29.12.95, Stephen Tweedie Support of BIGMEM added by Gerhard Wichert, Siemens AG, July 1999 Reshaped it to be a zoned
- Copyright (C) 1991-1994 Linus Torvalds zoned VM statistics
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- Copyright (C) 1991-1992,1995 Linus Torvalds Adapted for PowerPC (PReP) by Gary Thomas Modified by Cort Dougan (cort@cs.nmt.edu). Copied and modified from arch/i386/kernel/time.c
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- Copyright (C) 1991-1992 Linus Torvalds 64-bit file support on 64-bit platforms by Jakub Jelinek (jj@sunsite.ms.mff.cuni.cz)
- Copyright (C) 1991-1992 Linus Torvalds Added devfs support. Jan-11-1998, C. Scott Ananian Shared /dev/zero mmappping support, Feb 2000, Kanoj Sarcar
- Copyright (C) 1991-1992 Linus Torvalds Added support for a Unix98-style ptmx device. -- C. Scott Ananian , 14-Jan-1998
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- Copyright (C) 1991-1992 Linus Torvalds affs symlink handling code
- Copyright (C) 1991-1992 Linus Torvalds Based on bootsect.S and setup.S modified by more people than can be counted
- Copyright (C) 1991-1992 Linus Torvalds Big-endian to little-endian byte- swapping/bitmaps by David S. Miller (davem@caip.rutgers.edu), 1995
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- Copyright (C) 1991-1992 Linus Torvalds coh/dir.c
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- Copyright (C) 1991-1992 Linus Torvalds Enhanced CPU detection and feature setting code by Mike Jagdis and Martin Mares, November 1997
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- Copyright (C) 1991-1992 Linus Torvalds Modified to make sys_syslog() more flexible: added commands to return the last 4k of kernel messages, regardless of whether they've been read or no
- Copyright (C) 1991-1992 Linus Torvalds More specifically, the first copied function was strtobool, which was introduced by:
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- Copyright (C) 1992-1997,1999-2000 Silicon Graphics, Inc
- Copyright (C) 1992-1997,2000 Silicon Graphics, Inc. gda.h -- Contains the data structure for the global data area, The GDA contains information communicated between the PROM, SYMMON, an
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- Copyright (C) 1992,1998-2004 Linus Torvalds, Ingo Molnar This file contains the interrupt probing code and driver APIs
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- Copyright (C) 1992 David Giller, rafetmad@oxy.edu 1994, 1995 Eberhard Mönkeberg, emoenke@gwdg.de 1996 David van Leeuwen, david@tm.tno.nl
- Copyright (C) 1992 Doug Evans coh/alloc.c
- Copyright (C) 1992 Doug Evans coh/inode.c
- Copyright (C) 1992 Drew Eckhardt Generic hard disk header file by Drew Eckhardt
- Copyright (C) 1992 Drew Eckhardt Linux scsi disk driver by Drew Eckhardt Modified by Eric Youngdale ericy@andante.org to add scatter-gather, multip
- Copyright (C) 1992 Drew Eckhardt SCSI disk driver header file by Drew Eckhardt
- Copyright (C) 1992 Eric Youngdale Simulate a host adapter with 2 disks attached. Do a lot of checking to make sure that we are not getting blocks mixed up, and PANIC if anything out of t
- Copyright (C) 1992 Krishna Balasubramanian
- Copyright (C) 1992 Krishna Balasubramanian and Linus Torvalds
- Copyright (C) 1992 Krishna Balasubramanian Removed all the remaining kernel mess Catch the -EFAULT stuff properly Use GFP_KERNEL for messages as in 1.2 Fixed up the unchecked user spac
- Copyright (C) 1992 Krishna Balasubramanian Sep 1997 - Call suser() last after
- Copyright (C) 1992 Lawrence Foard Version 2 and 3 extensions to driver:
- Copyright (C) 1992 Linus Torvalds Adapted from arch/i386 by Gary Thomas
- Copyright (C) 1992 Linus Torvalds Rewritten. Old one was good in 2.2, but in 2.3 it was immoral. --ANK (990903)
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- Copyright (C) 1998,2000 Rik van Riel Thanks go out to Claus Fischer for some serious inspiration and for goading me into coding this file..
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- Copyright (C) 1998-2000 Russell King Integraphics Cyber2000 frame buffer device
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- Copyright (C) 2002 Pete Zaitcev The algorithm to search a zero bit string is geared towards its application. We expect a couple of fixed sizes of requests, so a rotating c
- Copyright (C) 2002 Pete Zaitcev (zaitcev@yahoo.com) We used to warn all over the code: DO NOT USE prom_printf(), and yet people do. Anton's banking code was outputting banks with prom_prin
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- Copyright (C) 2019 Facebook ----- START-EXPECTED-OUTPUT ----- enum e1 { A = 0, B = 1
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- (C) and server (S): Setup of client connection:
- (C) Architecture Software Developer's Manual, Volume 3: System Programming Guide, revision 12 in Table B-1: MSRs in the Pentium 4 and Intel Xeon Processors, on page B-4 and B-5. fsb\_
- (C) A(W1) C(C) Which would create W1-C-W1 dependencies, even though there is no actual deadlock possible. There are two solutions, using a read-recursive acquire on the wor
- (C) Balbir Singh, IBM Corp. 2006
- (C) Benjamin Reed
- (C) bits in a HPT entry can be set by hardware at any time up until the HPTE is invalidated and the TLB invalidation sequence has completed. This means that when removing a HPTE
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- Copyright (C) message. 0.0.19 Added I2C and SPI registers. Filled in interrupt enable. 0.0.20 Added GPIO info for SB Live 24bit. 0.0.21 Implement support for Line-in capture o
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- Copyright (C) 2015 Google Inc. bool ConvolutionFilter::ComputeResizeFilter(ResizeMethod aResizeMethod, int32\_t aSrcSize
- (C) 8::codeunit\_t codeunit\_t;
- (C) 8::validate(s,e); }
- (C) AFCET- % Gauthier-Villars % Norman Buckle, UQAH (nb; many additions) % Michael Ferguson, INRS-Telecommunications (mjf) June 1988 % Justi
- (C) and (B) win.gBrowser.removeTab(tab\_C); win.gBrowser.removeTab(tab\_B);
- (C) and Flip (F) bits are set in the CVO byte. TEST\_F(RtpSenderVideoTest, SendVideoWithCameraAndFlipCVO) {
- (C) below are standard double-angle formulae. (D) is a lesser known result and is derived below:
- (C)eBands j+1 -m-eBands j ) (alloc\_trim-5-LM) (end-j-1) (1(LM+BITRES))6; Giving less resol
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- (C) finishes. Store job begins. storeDone() finishes. Storing of A complete. Storing of C complete. We're done! Call onStoreCompleted. store(B) finishes... uh oh. In oth
- (C) from above:
- (C) fx5800p. Available under CC-BY-SA. No warranty. lightweightThemes.recommended-4.name=Pastel Gradient
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- (C) int mNum; void act(); };
- (C) is false, return false. 2\ If C has a BoundTargetFunction internal slot, then a. Let BC be the value of C's BoundTargetFunction internal slot. b. Return InstanceofOperato
- (C) is false, return false. features: Symbol.hasInstance \---
- (C) is false, throw a TypeError exception. \---
- (C) is false, throw a TypeError exception. features: Array.prototype.flatMap \---
- (C) is false, throw a TypeError exception. features: Array.prototype.flatten \---
- (C) is false, throw a TypeError exception. features: Symbol.hasInstance \---
- (C) is false, throw a TypeError exception. features: Symbol.species \---
- (C) is false, throw a TypeError exception. ... includes: testTypedArray.js features: TypedArray
- (C) is not Object, throw a TypeError exception. 2\ Let instOfHandler be GetMethod(C, @@hasInstance). 3\ ReturnIfAbrupt(instOfHandler). 4\ If instOfHandler is not undefined, then a. R
- (C) is not Object, throw a TypeError exception. 2\ Let instOfHandler be GetMethod(C, @@hasInstance). 3\ ReturnIfAbrupt(instOfHandler). features: Symbol.hasInstance \---
- (C) is not Object, throw a TypeError exception. 3\ Let promiseCapability be ? NewPromiseCapability(C). ... features: Symbol.species
- (C) is not Object, throw a TypeError exception. 6\ Let S be Get(C, @@species). 7\ ReturnIfAbrupt(S). 8\ If S is either undefined or null, return defaultConstructor. 9\ If IsConstructor
- (C) is not Object, throw a TypeError exception. 6\ Let S be Get(C, @@species). 7\ ReturnIfAbrupt(S). 8\ If S is either undefined or null, return defaultConstructor. features: Symbol.split
- (C) is not Object, throw a TypeError exception. 6\ Let S be Get(C, @@species). 7\ ReturnIfAbrupt(S). features: Symbol.split, Symbol.species \---
- (C) is not Object, throw a TypeError exception. ... features: Symbol

- (C) is not Object, throw a TypeError exception. features: Symbol \---
- (C) is not Object, throw a TypeError exception. features: Symbol.split \---
- (C) is not Object, throw a TypeError exception. ... includes: testTypedArray.js features: Symbol, TypedArray
- (C) is Object, then a. Let C be ? Get(C, @@species). b. If C is null, let C be undefined. ...
- (C) is Object, then a. Let C be ? Get(C, @@species). b. If C is null, let C be undefined. ... 10\ Return ? Construct(C, « length »). features: Symbol.species
- (C) is Object, then a. Let C be ? Get(C, @@species). b. If C is null, let C be undefined. 8\ If C is undefined, return ? ArrayCreate(length)
- (C) is Object, then a. Let C be ? Get(C, @@species). b. If C is null, let C be undefined. 8\ If C is undefined, return ? ArrayCreate(length). features: Symbol.species \---
- (C) is Object, then a. Let C be ? Get(C, @@species). features: Symbol.species \---
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- (C) is true, then a. Let A be ? Construct(C, « len »). ...
- (C) is true, then a. Let A be Construct(C, «len»). ..
- (C) is true, then a. Let A be Construct(C, «len»). ... 11\ Return A
- (C) is true, then a. Let A be Construct(C, «len»). 13\ Else, a. Let A be ArrayCreate(len). ... 19\ Return A
- (C) is true, then a. Let A be Construct(C, «len»). 13\ Else, b. Let A be ArrayCreate(len). 14\ ReturnIfAbrupt(A). 15\ Let k be 0. 16\ Repeat, while k len a. Let Pk be ToString(k)
- (C) is true, then a. Let A be Construct(C, «len»). 5\ Else, b. Let A be ArrayCreate(len). ... 11\ Return A
- (C) is true, then a. Let A be Construct(C, «len»). 5\ Else, b. Let A be ArrayCreate(len). 6\ ReturnIfAbrupt(A). ..
- (C) is true, then a. Let thisRealm be the current Realm Record. b. Let realmC be ? GetFunctionRealm(C). c. If thisRealm and realmC are not the same Realm Record, then i
- (C) is true, then i. Let A be ? Construct(C). ..
- (C) is true, then i. Let A be Construct(C). b. Else, i. Let A be ArrayCreate(0). c. ReturnIfAbrupt(A). features: Symbol.iterator \---
- (C) meth = getattr(C, method\_name)
- (C) min = Cmin max = Cmax if n 0.0 C = l + ( ( ( C - l ) x l ) / ( l - min ) ) if x 1.0 C = l + ( ( ( C - l ) x ( 1 - l ) ) / ( max - l ) ) return C se
- (C) min = Cmin max = Cmax if n 0.0 C = l + (((C - l) x l)/(l - min)) if x 1.0 C = l + (((C - l) x (1 - l))/(max - l)) return C set\_lum (C, l):
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- Copyright (C) 10 define NP\_INFO\_OriginalFilename 11
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- Copyright (C) string. uprv\_memset(dest + sizeof(header), 0, headerSize - (int32\_t)sizeof(header)); dest += headerSize; capacity -= headerSize; } else {
- Copyright (C) symbol, for example. The -1 is a bug fix from Rob Laveaux. It keeps an overflow from happening if there is no ';'. There are actually 2 ways to exit this loop -
- Copyright (C) symbol let request1 = new RESTRequest(server.baseURI +
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- (c) there is clear notice in each modified Data File or in the Software as well as in the documentation associated with the Data File(s) or Software that the data or software has been modified
- (C) to a UErrorCode first test if(U\_FAILURE(errorCode)) { return immediately; } so that in a chain of such functions the first one that sets an error code causes the following ones to not per
- (C) with no optimizations should not display any opts. yield checkFrame(3);

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- (C) if u1 = x break elif u1 = \_exp(-x): == (E) break return x beta First, we want (A) to b
- (C) is False and we take the elif branch, (E). For it to be True, so that the break is executed, we need that:
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- Copyright (C) Precompiled header for standard bootstrapper application.-----
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- (C) P.register(C) self.assertIsInstance(C(), P) self.assertIsInstance(C(), D)
- (C) self.assertEqual(len(h), orig\_len - 1)
- (C) self.assertEqual(len(h), orig\_len - 3)
- (C) self.assertIn(( self.assertIn(('c', 'method', C), attrs, 'missing plain method') self.assertIn(('p', 'property', A), attrs, 'missing property') self.assertIn(('m'
- (C) self.assertIsSub
- (C) self.assertTrue(issub
- (C) Set-Cookie: keebler=
- (C) Set-Cookie: oreo=doublestuff; Path=
- (C) should pass silently self.assertRaises(RuntimeError, C.register, A) cycles not allowed C.register(B) ok
- (C) These commands and their arguments will be passed to a C function in the :mod:`\_tkinter` - note the underscore - extension module
- (C) The Tk part of the Tk Widgets implement the final mapping to ..
- (C) the Xlib library to draw graphics on the screen
- (C) True, (D) False
- (C) with self.assertRaisesRegex(TypeError, asdict(int)
- (C) with self.assertRaisesRegex(TypeError, astuple(int)
- (C) with self.assertRaisesRegex(TypeError, fields(C()))
- (C) with self.assertRaises(TypeError): issub

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- (C) AS select(S). sqlite3CreateView(pParse, &X;, &Y;, &Z;, C, S, T, E); } cmd ::= DROP VIEW ifexists(E) fullname(X). { sqlite3DropTable(pParse, X, 1, E);
- (C) JOIN. {X = sqlite3JoinType(pParse,&A;,&B;,&C;);X-overwrites-A}
- (C) KiB in size. mxCache = mxCache -1024; }else{ mxCache = mxCache pgsg; } mxCache = MIN(mxCache, SQLITE\_MAX\_PMASZ); pSorter-mxPmaSize = MAX(pSo
- (C) language ==
- Copyright (C) All of the deliverable code in SQLite has been dedicated to the
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- (C) ruleid ==
- (C) sortorder(Z). A = parserAddExprldListTerm(pParse, 0, &Y;, C, Z); A-overwrites-Y }
- (C) sortorder(Z). A = parserAddExprldListTerm(pParse, A, &Y;, C, Z); }
- (C) (strlen(C) char sqlite3ColumnType(Column ,char ); define sqlite3StrNICmp sqlite3\_strnicmp
- (C) tablename = (D) idcolumn = \$idcolumn (E) parentcolumn = \$parentcolumn
- (C) The content of every column in the row
- (C) Unique index that do use OE\_Replace by default. The ordering of (2) and (3) is accomplished by making sure the linked list of indexes attached to a table puts all OE\_Replace indexe

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## STMicroelectronics SLA0048

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SLA0048 Rev4/March 2018

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(busybox:1.31.1, Bzip2:1.0.8)

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### (D-Bus:1.12.24)

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### (ICU for C/C++ (ICU4C):66.1)

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Version 1, February 1989

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