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- (C) self.assertEqual(len(ids), 1) Note canonicalized names self.assertEqual(GObject.signal_name(ids 0), 'my-signal') There is no signal 0 in gobject self.a

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- Copyright (C) 2001 Clemson University and The University of Chicago See COPYING in top-level directory
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- Copyright (C) 2005-2006 Red Hat Inc Alan Cox based upon cs5535.c from AMD as cleaned up and made readable and Linux style by Wolfgang Zuleger w
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- Copyright (C) 2006-2009 VIA Technology, Inc
- Copyright (C) 2006-2012 include include include include include include include net/addrc
- Copyright (C) 2006-2012 include include include include include include include linux/export
- Copyright (C) 2006-2012 Patrick McHardy Based on the old ipv4-only ipt_ULOG.c:
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- Copyright (C) 2007 Pavel Emelyanov , OpenVZ, SWsoft Inc

- Copyright (C) 2007 Red Hat Inc Based upon linux/drivers/ide/pci/cmd640.c Version 1.02 Sep 01, 1996
- Copyright (C) 2007 Red Hat, Inc.
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- Copyright (C) 2010-2011 Thomas Renninger, Novell Inc. Ideas taken over from the perf userspace tool (included in the Linus kernel git repo): subcommand builtins and param parsin
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- Copyright (C) 2010-2011 Thomas Renninger , Novell Inc. Output format inspired by Len Brown's turbostat tool
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- Copyright (C) 2015 Cyril Bur, IBM Corp. This test attempts to see if the FPU registers change across preemption. Two things should be noted here a) The check_fpu function in asm only checks t
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- Copyright (C) 2015 Cyril Bur, IBM Corp. This test attempts to see if the VMX registers change across preemption. Two things should be noted here a) The check_vmx function in asm only checks t
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 happened so this test just uses many thr
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- Copyright (C) 2015 Jonas Gorski include include include include include include linux/spin

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- Copyright (C) 2015 Linaro Limited. Author: Stanimir Varbanov
- Copyright (C) 2015 Linaro Ltd. include include include include include include include include include t
- Copyright (C) 2015-2018 Linaro Ltd. include include include include include include
- Copyright (C) 2015,2017 Linaro Ltd. include include include include
- Copyright (C) 2015 Linaro Ltd. undef TRACE_SYSTEM define TRACE_SYSTEM greybus
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- Copyright (C) 2015 Michael Neuling, IBM Corp. Edited: Rashmica Gupta, Nov 2015
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- Copyright (C) 2015 Michael Neuling, IBM Corp. Test the kernel's signal delievery code to ensure that we don't trelaim twice in the kernel signal delivery code. This can happen if we trigger
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- Copyright (C) 2014-2015 Pengutronix, Markus Pargmann
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- Copyright (C) 2016 Cyril Bur, IBM Corp. Sending one self a signal should always get delivered
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- Copyright (C) 2016 David Lechner Based on iio/ad7923.c:
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- Copyright (C) 2017 Dong Aisheng
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- Copyright (C) 2017 Google Inc Provides a simple driver to control the ASPEED LPC snoop
- Copyright (C) 2017 Google, Inc This driver uses the memory mapper cros-ec
- Copyright (C) 2017 Google LLC Based on Intel IPU4 driver
- Copyright (C) 2017 Gustavo Romero, Breno Leitao, Cyril Bur, IBM Corp. Force FP, VEC and VSX unavailable exception during transaction in all possible scenarios regarding the MSR.FP and MSR.VEC st
- Copyright (C) 2017 Gustavo Romero, IBM Corp. Check if thread endianness is flipped inadvertently to BE on trap caught in TM whilst MSR.FP and MSR.VEC are zero (i.e. just after load_fp and loa
- Copyright (C) 2017 Hannes Reinecke, SUSE Linux GmbH Based on the original DAC960 driver
- Copyright (C) 2017-2018 Hannes Reinecke, SUSE Linux GmbH Based on the original DAC960 driver, which has
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- Copyright (C) 2017 IBM Corp. include include include include include include asm/
- Copyright (C) 2017,2019 IBM Corp. include include
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- Copyright (C) 2017 IBM Corporation cpufeatures is the new way to discover CPU features with /cpus/features devicetree. This supersedes PVR based discovery (

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- Copyright (C) 2017 IBM Corp. undef TRACE_SYSTEM define TRACE_SYSTEM ocxl
- Copyright (C) 2017 Impinj, Inc Author: Andrey Smirnov Based on the code of analogus driver:
- Copyright (C) 2017 John Sperbeck Test that an access to a mapped but inaccessible area causes a SEGV and reports si_code == SEGV_ACCERR
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- Copyright (C) 2017 Manivannan Sadhasivam IIO driver for VL6180 (7-bit I2C slave address 0x29)
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- Copyright (C) 2017 Nuvoton Technology Crop
- Copyright (C) 2017 Nuvoton Technology Crop. Author: David Lin Co-author: John Hsu Co-author: Seven Li
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- Copyright (C) 2018-2019 NXP. Limitations:
- Copyright (C) 2018-2019 NXP Semiconductors include include include include soc/mscc/oce
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- MSR TS being set, the kernel will forc • Copyright (C) 2018 Breno Leitao, IBM Corp
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- Copyright (C) 2018 DEVIALET
- Copyright (C) 2018 Google Inc. Author: Eric Dumazet (edumazet@google.com) Reference program demonstrating tcp mmap() usage, and SO_RCVLOWAT hints for receiver
- Copyright (C) 2018 Google Inc. Author: Soheil Hassas Yeganeh (soheil@google.com) Simple example on how to use TCP_INQ and TCP_CM_INQ
- Copyright (C) 2016,2018 Google, Inc This driver uses the cros-ec
- Copyright (C) 2018 Google LLC. Author: Tomasz Figa Author: Yong Zhi
- Copyright (C) 2018 Google LLC. Author: Tuukka Toivonen Author: Sakari Ailus Author: Samu Onkalo Author: To
- Copyright (C) 2018 Google LLC. Driver for Semtech's SX9310/SX9311 capacitive proximity/button solution. Based on SX9500 driver and Semtech driver using the input framework https://my.syncplic
- Copyright (C) 2018 Google LLC The corresponding platform device is typically registered in drivers/platform/chrome/wilco_ec/core.c
- Copyright (C) 2018 Google LLC The Wilco EC is similar to a typical ChromeOS embedded controller. It uses the same MEC based low-level communication
 and a similar protocol, but with some import
- Copyright (C) 2018 Google LLC This is the entry point for the drivers that control the Wilco EC
- Copyright (C) 2014,2018 Google LLC. Tomasz Figa
- Copyright (C) 2018 Google LLC. Tomasz Figa Based on s5p-mfc driver by Samsung Electronics Co., Ltd
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- Copyright (C) 2018 IBM Corporation Author: Anshuman Khandual
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- · Copyright (C) 2018 Lubomir Rintel include include include include include include
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- · Copyright (C) 2018 Michael Ellerman, IBM Corp. Test that an out-of-bounds branch to counter behaves as expected
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- · Copyright (C) 2018 NXP Author: Dong Aisheng Implementation of the SCU IPC functions using MUs (client side)
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- Copyright (C) 2019 ARM Limited 32 Bit compat layer by Stefani Seibold sponsored by Rohde & Schwarz GmbH & Co. KG Munich/Germany
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- Copyright (C) 2019 Dan Robertson I2C address is either 0x14 or 0x15 depending on SDO
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- Copyright (C) 2019 dvanced Micro Devices, Inc
- Copyright (C) 2019 Finn Thain derived in part from:
- Copyright (C) 2019 Google, Inc. ChromeOS Embedded Controller codec driver
- Copyright (C) 2019 Google, LLC
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- Copyright (C) 2019 Google LLC See Documentation/ABI/testing/debugfs-wilco-ec for usage
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- Copyright (C) 1992 Krishna Balasubramanian Removed all the remaining kerneld mess Catch the -EFAULT stuff properly Use GFP_KERNEL for messages as in 1.2 Fixed up the unchecked user spac
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- Copyright (C) 2019 ARM Limited Try to mangle the ucontext from inside a signal handler, mangling the DAIF bits in an illegal manner: this attempt must be spotted by Kernel and the test cas
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- Copyright (C) 2019 Facebook ----- START-EXPECTED-OUTPUT ----- struct S { int S; int U;
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- (C) 11101: sent, recv_ack (B) or implicit
- (C) 2 1) : ((C)
- (C) Adam Belay. include include include include include include include
- (C) Alex Holden
- (C) and server (S): Setup of client connection:
- (C) Architecture Software Developer's Manual, Volume 3: System Prgramming Guide, revision 12 in Table B-1: MSRs in the Pentium 4 and Intel Xeon Processors, on page B-4 and B-5. fsb_
- (C) A(W1) C(C) Which would create W1-C-W1 dependencies, even though there is no actual deadlock possible. There are two solutions, using a
 read-recursive acquire on the wor
- (C) Balbir Singh, IBM Corp. 2006
- (C) Benjamin Reed
- (C) bits in a HPT entry can be set by hardware at any time up until the HPTE is invalidated and the TLB invalidation sequence has completed. This means that when removing a HPTE
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- (C) Nadia Yvette Chambers, April 2004 include include include include include include include
- (C) Neil Horman
- (C) Notes: To disconnect the current associated BSS Command Mode struct disconnect_parm { u32 rsvd;
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- Copyright (C) 0 define DVD_CPM_COPYRIGHTED 1
- Copyright (C) 0x0000004 /
- Copyright (C) 0x01 define DVD_STRUCT_DISCKEY 0x02 define DVD_STRUCT_BCA 0x03 define DVD_STRUCT_MANUFACT 0x04
- Copyright (C) 0xd4 define CX2341X_ENC_SET_EVENT_NOTIFICATION 0xd5 define CX2341X_ENC_SET_NUM_VSYNC_LINES 0xd6 define CX2341X_ENC_SET_PLACEHOLDER 0xd7 define CX2341X_ENC_MUTE_VIDEO 0xd9 define CX
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- (C) 8::codeunit_t codeunit_t;
- (C) 8::validate(s,e); }
- (C) AFCET- % Gauthier-Villars % Norman Buckle, UQAH (nb; many additions) % Michael Ferguson, INRS-Telecommunications (mjf) June 1988 % Justi
- (C) and (B) win.gBrowser.removeTab(tab_C); win.gBrowser.removeTab(tab_B);

- (C) and Flip (F) bits are set in the CVO byte. TEST_F(RtpSenderVideoTest, SendVideoWithCameraAndFlipCVO) {
- (C) below are standard double-angle formulae. (D) is a lesser known result and is derived below:
- (C)eBands j+1 -m-eBands j) (alloc_trim-5-LM) (end-j-1) (1(LM+BITRES))6; Giving less resol
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- (C) finishes. Store job begins. storeDone() finishes. Storing of A complete. Storing of C complete. We're done! Call onStoreCompleted. store(B) finishes... uh oh. In oth
- (C) from above:
- (C) fx5800p. Available under CC-BY-SA. No warranty. lightweightThemes.recommended-4.name=Pastel Gradient
- © chaals, and available under the W3C Software license
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- (C) int mNum; void act(); };
- (C) is false, return false. 2\. If C has a BoundTargetFunction internal slot, then a. Let BC be the value of C's BoundTargetFunction internal slot. b. Return InstanceofOperato
- (C) is false, return false. features: Symbol.hasInstance \---
- (C) is false, throw a TypeError exception. \---
- (C) is false, throw a TypeError exception. features: Array.prototype.flatMap \---
- (C) is false, throw a TypeError exception. features: Array.prototype.flatten \---
- (C) is false, throw a TypeError exception. features: Symbol.hasInstance \----
- (C) is false, throw a TypeError exception. features: Symbol.species \---
- (C) is false, throw a TypeError exception. ... includes: testTypedArray.js features: TypedArray
- (C) is not Object, throw a TypeError exception. 2\. Let instOfHandler be GetMethod(C, @@hasInstance). 3\. ReturnlfAbrupt(instOfHandler). 4\. If instOfHandler is not undefined, then a. R
- (C) is not Object, throw a TypeError exception. 2\. Let instOfHandler be GetMethod(C, @ @hasInstance). 3\. ReturnIfAbrupt(instOfHandler). features: Symbol.hasInstance \---
- (C) is not Object, throw a TypeError exception. 3\. Let promiseCapability be ? NewPromiseCapability(C). ... features: Symbol.species
- (C) is not Object, throw a TypeError exception. 6\. Let S be Get(C, @@species). 7\. ReturnIfAbrupt(S). 8\. If S is either undefined or null, return defaultConstructor. 9\. If IsConstructor
- (C) is not Object, throw a TypeError exception. 6\. Let S be Get(C, @@species). 7\. ReturnIfAbrupt(S). 8\. If S is either undefined or null, return defaultConstructor. features: Symbol.split
- (C) is not Object, throw a TypeError exception. 6\. Let S be Get(C, @@species). 7\. ReturnlfAbrupt(S). features: Symbol.split, Symbol.species \---
- (C) is not Object, throw a TypeError exception. ... features: Symbol
- (C) is not Object, throw a TypeError exception. features: Symbol \---
- (C) is not Object, throw a TypeError exception. features: Symbol.split \---
- (C) is not Object, throw a TypeError exception. ... includes: testTypedArray.js features: Symbol, TypedArray
- (C) is Object, then a. Let C be ? Get(C, @@species). b. If C is null, let C be undefined. ..
- (C) is Object, then a. Let C be ? Get(C, @@species). b. If C is null, let C be undefined. ... 10\. Return ? Construct(C, « length »). features: Symbol.species
- (C) is Object, then a. Let C be ? Get(C, @@species). b. If C is null, let C be undefined. 8\. If C is undefined, return ? ArrayCreate(length)
- (C) is Object, then a. Let C be ? Get(C, @@species). b. If C is null, let C be undefined. 8\. If C is undefined, return ? ArrayCreate(length). features: Symbol.species \---
- (C) is Object, then a. Let C be ? Get(C, @@species). features: Symbol.species \---
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- (C) is true, then a. Let A be ? Construct(C, « len »). ..
- (C) is true, then a. Let A be Construct(C, «len»). ..
- (C) is true, then a. Let A be Construct(C, «len»). ... 11\. Return A
- (C) is true, then a. Let A be Construct(C, «len»). 13\. Else, a. Let A be ArrayCreate(len). ... 19\. Return A
- (C) is true, then a. Let A be Construct(C, «len»). 13\. Else, b. Let A be ArrayCreate(len). 14\. ReturnIfAbrupt(A). 15\. Let k be 0. 16\. Repeat, while k len a. Let Pk be ToString(k)
- (C) is true, then a. Let A be Construct(C, «len»). 5\. Else, b. Let A be ArrayCreate(len). ... 11\. Return A
- (C) is true, then a. Let A be Construct(C, «len»). 5\. Else, b. Let A be ArrayCreate(len). 6\. ReturnlfAbrupt(A). ...
- (C) is true, then a. Let thisRealm be the current Realm Record. b. Let realmC be ? GetFunctionRealm(C). c. If thisRealm and realmC are not the same Realm Record, then i
- (C) is true, then i. Let A be ? Construct(C). ..
- (C) is true, then i. Let A be Construct(C). b. Else, i. Let A be ArrayCreate(0). c. ReturnlfAbrupt(A). features: Symbol.iterator \---
- (C) meth = getattr(C, method_name)
- (C) min = Cmin max = Cmax if n 0.0 C = I + (((C-I) × I)/(I min)) if x 1.0 C = I + (((C-I) × (1-I))/(max I)) return C se
- (C) min = Cmin max = Cmax if n 0.0 C = I + (((C − I) × I)/(I − min)) if x 1.0 C = I + (((C − I) × (1 − I)) (max − I)) return C set_lum (C, I):
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- Copyright (C) MOZ_Z_inflate_copyright define inflate_fast MOZ_Z_inflate_fast define inflate_table MOZ_Z_inflate_table define z_errmsg MOZ_Z_z_errmsg define zcalloc MOZ_Z_zcalloc define zcfree MOZ_Z_z
- Copyright (C) must be aligned to 4-bytes.
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- Copyright (C) return PNG_STRING_COPYRIGHT else ifdef __STDC__ return PNG_STRING_NEWLINE \
- Copyright (C) sig = cmsSigCopyrightTag; break;
- Copyright (C) SkEndian_SwapBE32(kTAG_cprt), SkEndian_SwapBE32(kTAG_cprt_Offset), SkEndian_SwapBE32(kTAG_cprt_Bytes), };
- Copyright (C) small
- Copyright (C) SOFTWARE, EVEN IF ADVISED OF THE POSSIBILITY OF SUCH DAMAGE
- Copyright (C) SPECIAL, EXEMPLARY, OR CONSEQUENTIAL DAMAGES (INCLUDING, BUT
- Copyright (C) status of your
- Copyright (C) string in the executable of your product
- Copyright (C) string. @stable ICU 2.4 define U_COPYRIGHT_STRING_LENGTH 128
- Copyright (C) string. uprv_memset(dest + sizeof(header), 0, headerSize (int32_t)sizeof(header)); dest += headerSize; capacity -= headerSize; } else {
- Copyright (C) symbol, for example. The -1 is a bug fix from Rob Laveaux. It keeps an overflow from happening if there is no ','. There are actually 2 ways to
 exit this loop -
- Copyright (C) symbol let request1 = new RESTRequest(server.baseURI +
- Copyright (C) tag memcpy(ptr, kCopyrightTagHeader, sizeof(kCopyrightTagHeader)); ptr += sizeof(kCopyrightTagHeader); memcpy(ptr, kCopyrightTagBody, sizeof(kCopyrightTagBody)); ptr += sizeo
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- Copyright (C) undef MY_VERSION_COPYRIGHT_DATE define MY_AUTHOR_NAME
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- Copyright (C) year. s { (?.)
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- Copyright (C) year which is autogenerated
- (C) SAT (r If we extend clip_color with an extra argument a and change
- (C) Sean.Martell. Available under CC-BY-SA. No warranty. lightweightThemes.recommended-2.name=Space Fantasy
- (C) storeDone() store(A) finishes. Store job begins
- (C) T; else F; iteratively constant-fold |F| after folding |C| and |T| (and possibly completely replacing the whole thing with |T| or |F|); otherwise fold F normally.
 M
- (c) there is clear notice in each modified Data File or in the Software as well as in the documentation associated with the Data File(s) or Software that the data or software has been modified
- (C) to a UErrorCode first test if(U_FAILURE(errorCode)) { return immediately; } so that in a chain of such functions the first one that sets an error code causes
 the following ones to not per
- (C) with no optimizations should not display any opts. yield checkFrame(3);

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- (C) evaluates to True. We now encounter a second if statement, (D), which in order to execute must satisfy the following condition:
- (C) functions get called by the interpreter when, for instance, an attribute gets looked up on an object, a method called, or it is multiplied by another object. These C functions are called
- (C) if u1 = x break elif u1 = _exp(-x): === (E) break return x beta First, we want (A) to b
- (C) is False and we take the elif branch, (E). For it to be True, so that the break is executed, we need that:
- (C) isn objects reachable from the dying object (c1) isn't enough to stop
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- (C) P.register(C) self.assertIsInstance(C(), P) self.assertIsInstance(C(), D)
- (C) self.assertEqual(len(h), orig_len 1)
- (C) self.assertEqual(len(h), orig_len 3)
- (C) self.assertln((self.assertln(('c', 'method', C), attrs, 'missing plain method') self.assertln(('p', 'property', A), attrs, 'missing property') self.assertln(('m'
- (C) self.assertIsSub
- (C) self.assertTrue(issub
- (C) Set-Cookie: keebler=
- (C) Set-Cookie: oreo=doublestuff; Path=/
- (C) should pass silently self.assertRaises(RuntimeError, C.register, A) cycles not allowed C.register(B) ok
- (C) These commands and their arguments will be passed to a C function in the :mod:`_tkinter` note the underscore extension module
- (C) The Tk part of the Tk Widgets implement the final mapping to ..
- (C) the Xlib library to draw graphics on the screen
- (C) True, (D) False
- (C) with self.assertRaisesRegex(TypeError, asdict(int)
- (C) with self.assertRaisesRegex(TypeError, astuple(int)
- (C) with self.assertRaisesRegex(TypeError, fields(C())
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by James Random Hacker.

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Ty Coon, President of Vice

That's all there is to it!

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(mozjs60:60.9.0, Readline:8.0)

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